

FE and iO for Turing Machines from Minimal Assumptions

Shweta Agrawal¹ and Monosij Maitra²

¹ IIT Madras, India

shweta.a@cse.iitm.ac.in

² IIT Madras, India

monosij@cse.iitm.ac.in

Abstract. We construct Indistinguishability Obfuscation (iO) and Functional Encryption (FE) schemes in the Turing machine model from the minimal assumption of compact FE for circuits (CktFE). Our constructions overcome the barrier of sub-exponential loss incurred by all prior work. Our contributions are:

1. We construct iO in the Turing machine model from the same assumptions as required in the circuit model, namely, sub-exponentially secure FE for circuits. The previous best constructions [41, 6] require sub-exponentially secure iO for circuits, which in turn requires sub-exponentially secure FE for circuits [5, 15].
2. We provide a new construction of single input FE for Turing machines with unbounded length inputs and optimal parameters from *polynomially* secure, compact FE for circuits. The previously best known construction by Ananth and Sahai [7] relies on iO for circuits, or equivalently, sub-exponentially secure FE for circuits.
3. We provide a new construction of multi-input FE for Turing machines. Our construction supports a fixed number of encryptors (say k), who may each encrypt a string x_i of *unbounded* length. We rely on sub-exponentially secure FE for circuits, while the only previous construction [10] relies on a strong knowledge type assumption, namely, public coin differing inputs obfuscation.

Our techniques are new and from first principles, and avoid usage of sophisticated iO specific machinery such as positional accumulators and splittable signatures that were used by all relevant prior work [41, 7, 6].

1 Introduction

The notion of indistinguishability obfuscation (iO) [11] seeks to garble programs such that the obfuscations of any two functionally equivalent programs are indistinguishable. While non-obvious at first what such a guarantee is good for, iO has emerged as a surprisingly powerful notion in cryptography, leading to many advanced cryptographic applications that were previously out of reach [27, 50, 23, 22, 12, 41, 14, 44, 26, 24, 46].

Functional encryption (FE) [49, 16, 48] is a generalization of public key encryption that enables fine grained access control on encrypted data. In FE, a secret key corresponds to a function f and ciphertexts correspond to strings from the domain of f . Given a function key SK_f and a ciphertext CT_x , the decryptor learns $f(x)$ and nothing else.

While an important primitive in its own right, FE has also been shown to imply iO, albeit with sub-exponential loss [5, 15]. Over the last few years, both primitives have received significant attention, with a rich body of work that attempts to support more general models of computation [12, 22, 41, 20, 25, 4, 21], rely on weaker assumptions [19, 30, 42, 13, 40, 8, 43, 45, 37, 38, 39], achieve stronger security [3, 19] and greater efficiency [6].

In this work, we make further progress towards the goal of basing iO and FE on minimal assumptions, in the Turing machine model of computation. This question has been studied extensively [34, 7, 12, 41, 22, 25, 20, 4, 21, 6] – we refer the reader to [7, 6] for a detailed discussion. Below, we summarize the state of art:

1. iO for Turing Machines with unbounded memory and bounded inputs are constructed in the works of Koppula et al. and Ananth et al. [41, 6]. Both works rely on the existence of sub-exponentially secure

iO for circuits along with other standard assumptions. We note that FE for circuits implies iO with sub-exponential loss, so when relying on FE for circuits, these works incur double sub-exponential loss.

2. For *single input* FE for Turing machines that accept unbounded length inputs and place no restriction on the description size or space complexity of the machine, the state of art is the work of Ananth and Sahai [7], which relies on the existence of iO for circuits.
3. For *multi-input* FE in the Turing machine model, the only known construction is [10], which relies on the existence of public coin differing inputs obfuscation (diO).

Our Results. We construct Indistinguishability Obfuscation (iO) and Functional Encryption (FE) schemes in the Turing machine model from the minimal assumption of compact FE for circuits (CktFE). Our constructions overcome the barrier of sub-exponential loss incurred by all prior work. Our contributions are:

1. We construct iO for Turing machines with bounded inputs and unbounded memory from the same assumptions as required by iO for circuits, namely, sub-exponentially secure FE for circuits. The previous best constructions [41, 6] require sub-exponentially secure iO for circuits, which in turn requires sub-exponentially secure FE for circuits [5, 15], resulting in double sub-exponential loss.
2. We provide a new construction of single input FE for Turing machines with unbounded inputs, achieving optimal parameters from *polynomially* secure, compact FE for circuits. The previously best known construction by Ananth and Sahai [7] relies on iO for circuits, or equivalently, sub-exponentially secure FE for circuits. We note that iO for circuits implies decomposable compact FE for circuits [27] (please see the full version [1]), so our construction also implies FE for TMs from iO for circuits.
3. We provide a new construction of multi-input FE for Turing machines. Our construction supports a fixed number of encryptors (say k), who may each encrypt a string x_i of *unbounded* length. We rely on sub-exponentially secure FE for circuits, while the only previous construction [10] relies on a strong knowledge type assumption, namely, public coin differing inputs obfuscation. The arity k supported by our scheme depends on the underlying multi-input CktFE scheme, for instance using [40], we can support $k = \text{polylog}(\lambda)$.

Our constructions make use of FE for circuits that satisfy a mild property called *decomposability*, which in turn can be constructed generically from FE for circuits (please see Appendix A). Decomposable FE, analogously to decomposable randomized encodings [9], roughly posits that a long string be encrypted bit by bit using shared randomness across bits. This property is already satisfied by all known constructions of CktFE in the literature to the best of our knowledge.

Our techniques are new and from first principles, and avoid usage of sophisticated iO specific machinery such as positional accumulators and splittable signatures that were used by all prior work [41, 7, 6]. Our work leverages the security notion of distributional indistinguishability (DI) for CktFE which was first considered by [31], who provided a construction for single input FE satisfying DI security assuming the existence of iO. We strengthen this result by constructing DI secure CktFE from standard CktFE. Please see Figure 1 for an overview of our results.

Additional Prior Work. Since iO is considered an inherently sub-exponential assumption and much stronger than the polynomial assumption of compact FE, replacing iO by FE in cryptographic constructions has already been studied extensively, for instance in the context of PPAD hardness [28], multi-input FE for circuits [19, 40] as well as trapdoor one-way permutations and universal samplers [29]. We note that aside from reliance on weaker, better understood assumptions, avoiding sub-exponential loss results in significantly more efficient schemes. We refer the reader to [29] for a detailed discussion.

Distributional indistinguishability was also considered in the context of output compressing randomized encodings [44]; indeed, this work implies that achieving DI security for FE for Turing

machines with *long* outputs is impossible in the plain model. We note that our construction sidesteps this lower bound by considering Turing machines with a single output bit.

iO for TMs with unbounded memory has been constructed by [41, 6] as discussed above, other prior works were limited to bounded space constraints. We note that [6] additionally achieve constant overhead in the size of the obfuscated program as well as amortization, which we do not consider in this work. We also note that the work of [10] achieve miFE for TMs where the number of encrypting parties can be arbitrary, whereas we only support a-priori fixed, bounded number of parties.

The approach of using decomposable FE for circuits to construct FE for deterministic finite automata (DFA) in the *single key* setting was suggested by [2]. In this work we develop and significantly generalize their ideas. In particular, we handle the unbounded key setting in FE for TMs which necessitates dealing with the much more complex indistinguishability style definition, for which we develop new proof techniques which use a novel “sliding trapdoor” approach and leverage distributional indistinguishability. In contrast, since [2] use simulation security for single key FE, their proof must not contend with any of these challenges. Please see below for details.

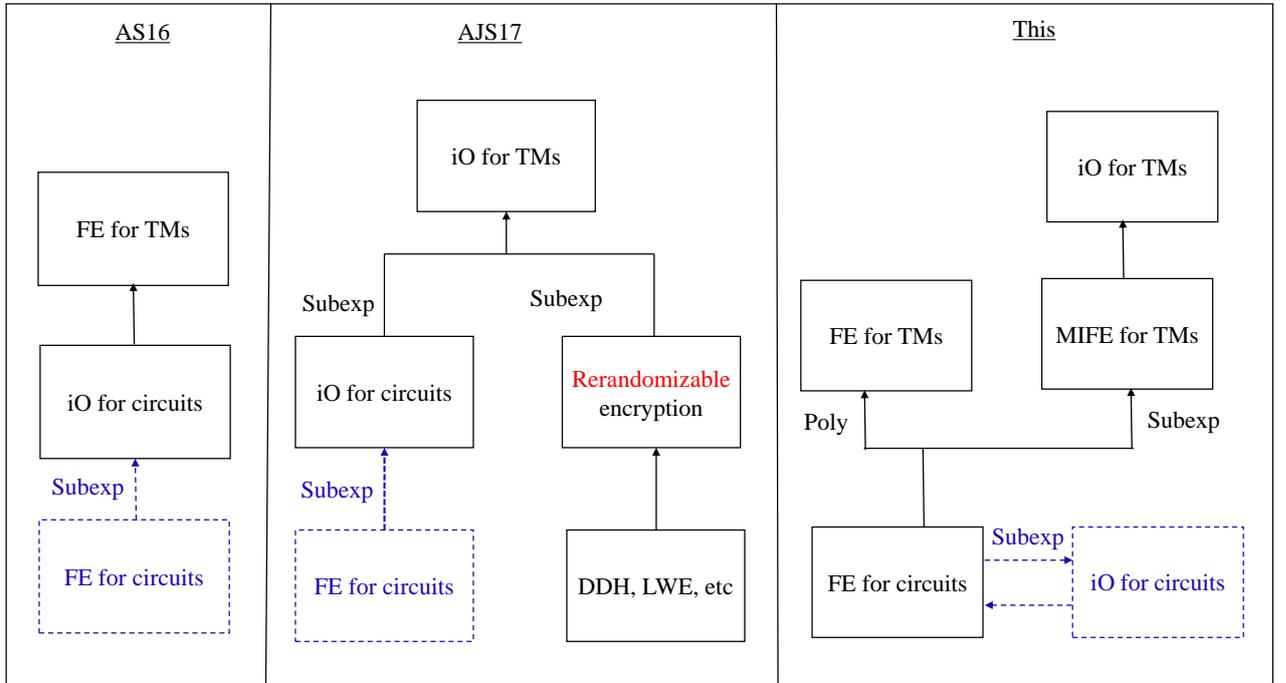


Fig. 1. Prior work and our results. The reductions with subexponential loss are specified, no specification implies standard polynomial loss. The dashed blue lines indicate primitives that are not actually used by the work in question; we add these to elucidate the relationship between primitives. We do not include [10] here since it relies on public coin iO.

Our Techniques. We describe an overview of our constructions, starting with single input FE, generalizing to multi-input FE and then building iO. All our constructions support the Turing machine model of computation. Our constructions rely on a single input FE scheme for *circuits*, denoted by CktFE, which satisfies *decomposability*. In Appendix A, we show that decomposable FE for circuits is implied by FE for circuits. Intuitively, decomposability means that the ciphertext CT_x for a multi-bit message x be decomposable into multiple ciphertext components CT_i for $i \in |x|$, one for each bit x_i of the message. Moreover, the ciphertext components encoding individual bits of a single input are tied together by common randomness, that is $CT_i = \mathcal{E}(\text{PK}, r, x_i)$ where \mathcal{E} is an encoding function and r is common

randomness used for all $i \in |\mathbf{x}|^3$. The notion of decomposability has been widely studied and used in the context of randomized encodings, which may be seen as a special case of functional encryption; please see [9] as an example.

Single Input TMFE. Recall that a Turing machine at any time step reads a symbol, state pair and produces a new symbol which is written to the work tape, a new state and a left or right head movement. By assuming the Turing machine is *oblivious*, the head movements of the TM may be fixed; thus, at any given time step when a work tape cell is read, we can compute the next time step when the same work tape cell will be accessed. This reduces the output at any time step t to a symbol, state pair, where the state is read in the next time step $t + 1$ and the symbol is read at a future (fixed) time step $t' > t$.

Our construction uses two CktFE schemes, $1FE_1$ and $1FE_2$, where $1FE_2$ is decomposable. Intuitively, $1FE_1$ is used by the encryptor to encode the unbounded length input, while $1FE_2$ is used to mimic the computation of the Turing machine, as we describe next. The ciphertext of $1FE_2$ is divided into two parts, encoding input components (t, σ) and q respectively. Here, t is the current time step in the computation and σ, q are the current work-tape symbol and state respectively. We maintain the invariant that at any time step t in the computation, both components of the ciphertext have been computed using common randomness derived from $\text{PRF}_K((t \parallel \text{salt}))$, where salt is an input chosen by the key generator and the PRF key K is chosen by the encryptor.

Now, to mimic the TM computation, we provide a function key for the Next functionality, that stores the transition table, receives as input the current (symbol, state) pair, computes the symbol to be written on the work tape and the next state using the transition table, derives the randomness using the PRF for the appropriate time step and outputs the encodings of the new (symbol, state) pair. In more detail, say the encryptor provides encodings of each input symbol x_i , for $i \in [|\mathbf{x}|]$, in addition to an encoding for the first (fixed) state q_{st} , where the encodings of $(1, x_1)$ and q_{st} share the same randomness so that they may be concatenated to yield a complete ciphertext for $(1, x_1, q_{\text{st}})$. Now, the function key may read input $(1, x_1, q_{\text{st}})$, lookup the transition table and produce an encryption of the next state q_2 and the symbol to be written x'_2 . The randomness used to encrypt q_2 is derived using a PRF as described above, and is the *same* as the randomness used by the encryptor to encode $(2, x_2)$. Hence, the two ciphertext components encoding $(2, x_2)$ and q_2 may be concatenated to yield a complete $1FE_2$ ciphertext which may be again decrypted using the function key.

Now consider how to support writing on tape. Say the symbol x'_2 will be read at future fixed time step t' . Then the function key encodes the tuple (t', x'_2) using randomness $\text{PRF}_K((t' \parallel \text{salt}))$. The state for time step t' , say q' is computed at time step $t' - 1$, also using randomness $\text{PRF}_K((t' \parallel \text{salt}))$. Thus, encodings of (t', x'_2) and q' may be joined together to yield a complete $1FE_2$ ciphertext which may be decrypted to propagate the computation.

A detail brushed away by the above description is that the encryptor, given input \mathbf{x} , cannot compute randomness generated by a PRF which has input a value salt chosen by the key generator. This is handled by making use of an additional scheme $1FE_1$, which re-encrypts ciphertexts provided by the encryptor via a ReRand functionality, using the requisite randomness. Note that we support inputs of unbounded length by leveraging the fact that CktFE schemes $1FE_1, 1FE_2$ support encryption of unbounded *number* of inputs, even if each must be of bounded length. Thus, the encryptor provides an unbounded number of $1FE_1$ ciphertexts which are rerandomized and translated to ciphertexts under $1FE_2$ using the ReRand function key provided by the key generator.

Encoding the PRF key. The above informal description hides an important detail – for the function key to produce ciphertext components using a PRF, it must have the key of the PRF, chosen by the encryptor⁴, passed to it as input. Thus the ciphertext must additionally encode the PRF key along with

³ Encoding of each bit may also use additional independent randomness, which is not relevant to the discussion here, and hence omitted.

⁴ Note that the PRF key must be encoded in the ciphertext rather than function key since it is required to be hidden.

inputs (t, x, q) . However, the ciphertext is constructed using randomness derived from the same PRF—resulting in circularity. We resolve this difficulty by using *constrained* PRFs [17, 36, 18], and having a ciphertext encode a PRF key that only allows computation of randomness for time steps *of the future*; this does not compromise its own security. For this constraint family, we provide a construction of cPRFs from standard assumptions. We believe this construction and the method of its application may be useful elsewhere⁵.

More formally, our construction makes use of constrained, delegatable PRF for the function family $f_t : \{0, 1\}^{2\lambda} \rightarrow \{0, 1\}$ defined as follows.

$$\begin{aligned} f_t(x||z) &= 1 \quad \text{if } x \geq t \\ &= 0 \quad \text{otherwise} \end{aligned}$$

We denote the constrained PRF key K_{f_t} by K_t for brevity. By the delegation property of constrained PRFs, we have that if $t' \geq t$ then $K_{t'}$ can be derived from K_t . The proof requires the PRF to be punctured at a fixed point in each hybrid, we provide a construction of delegatable punctured PRF in the full version of the paper [1].

Proof Overview. While the above description of single input TMFE is natural and intuitive, the proof of indistinguishability based security is quite subtle and requires new techniques as we discuss next. For ease of exposition, we describe the proof overview for the case where the adversary makes a single key request corresponding to some TM M . We must argue that the challenge ciphertext, which is a sequence of $1FE_1$ ciphertexts, together with ReRand and Next keys corresponding to a TM M , do not distinguish the bit b .

As discussed above, the $1FE_1$ ciphertexts are decrypted using the ReRand key to produce a sequence of $1FE_2$ ciphertexts, each corresponding to a time step in the TM execution (when the encoded symbol is read), which are in turn decrypted by Next keys to compute new $1FE_2$ ciphertexts for future time steps. We may view the $1FE_2$ ciphertexts as forming a chain, with each link of the chain corresponding to a single step of the TM computation, and each ciphertext producing (via decryption) a new ciphertext for the next time step, finally yielding the output when the TM halts (after T steps, say). Intuitively, since the output of the TM does not distinguish the bit b by admissibility of the TMFE adversary, we may argue by security of $1FE_2$ that the ciphertext at the penultimate step $T - 1$ also does not distinguish b , which implies that the ciphertext at step $T - 2$ hides b and so on, ultimately yielding indistinguishability of the entire chain, and hence of the $1FE_1$ challenge ciphertext.

Formalizing this intuitive argument is quite tricky. A natural approach would be to consider a sequence of hybrids, one corresponding to each link in the chain, and switch the $1FE_2$ ciphertexts one by one starting from the end of the chain. While intuitive, this idea is misleading – note that a naive implementation of this idea would lead to a chain which is “broken”: namely, its first links correspond to $b = 0$, and last links to $b = 1$. Since the ciphertext at a given step is decrypted to compute the ciphertext at the next step, a ciphertext corresponding to $b = 0$ cannot in general output a ciphertext for $b = 1$.

A standard approach to deal with this difficulty is to embed a “trapdoor” mode within the functionality [3, 5, 19] which lets us “hardwire” the ciphertexts that must be output by decryption directly in the key, allowing decryption to yield an inconsistent chain. However, this approach also fails in our case, since the length of the chain is unbounded and there isn’t sufficient space in the key to incorporate all its values.

Our Approach: “Sliding” Trapdoors. We deal with this difficulty by designing a novel “sliding-window” trapdoor approach which lets us hardwire the decryption chain “piece by piece”. In more detail, we start with the last two time steps $(T, T - 1)$, program the key to produce the output corresponding to $b = 1$ for time step T and $b = 0$ for $T - 1$, then transition to a world where the output corresponds to $b = 1$

⁵ For instance, a similar situation w.r.t circularity arises in the original garbled RAM construction of Lu and Ostrovsky [47].

for both T and $T - 1$. At this point, the hardwiring of the output for time step T is redundant, since the ciphertext output by the decryption process at time step $T - 1$ automatically computes the output corresponding to $b = 1$ at time step T . Thus, we may now *slide* the trapdoor to program to the next pair $(T - 1, T - 2)$, switching the decryption output at time step $T - 2$ to $b = 1$ and so on, until the entire chain corresponds to $b = 1$.

Intuitively, we are “programming” the decryption only for outputs at both ends of the “broken link”, so that preceding links are generated using $b = 0$ and subsequent links are generated using $b = 1$. We leverage the fact that the chain links corresponding to future time-steps are encoded *implicitly* in a given time step – hence if we manage to hide the chain inconsistency at a certain position i , this implies that the remainder of the chain is constructed using the bit encoded at step i . Formalizing this argument requires a great deal of care, as we must keep track of the “target” time steps corresponding to the two ends of the broken link that are being programmed, the time steps at which the symbol and state ciphertexts are generated to be “consumed” at the target time-steps, the particular values that must be encoded in the symbol, state fields in both cases as well as the key that is being handled at a given time in the proof. For more details, please see Section 3.3.

Generalising to Multi-Input FE for Turing machines. For the k party setting, a natural idea is to have each party encrypt its own input \mathbf{x}_i , and use a k input CktFE scheme kFE [19, 40], to “aggregate” these into the “input” ciphertext $\text{CT}(\mathbf{x})$ for one long input $\mathbf{x} = (\mathbf{x}_1 \parallel \mathbf{x}_2 \parallel \dots \parallel \mathbf{x}_k)$, under a different CktFE scheme 1FE. Note that the length of \mathbf{x} is unknown hence it may not be encoded “all at once” but must be encoded bit by bit as in the previous scheme. Now, by additionally providing the 1FE ciphertext encoding the start state of the Turing machine $\text{CT}(q_{\text{st}})$, and a function key to compute the transition table of the TM as in the previous scheme, we may proceed with the computation exactly as before.

Formalizing this idea must contend with several hurdles. In the multi-input setting, the i^{th} encryptor may encode multiple inputs and functionality permits “mix and match” of ciphertexts in the sense that any input encoded by party i may be combined with any input encoded by parties $j \in [k]$, $j \neq i$. Therefore, if each of k parties encodes T ciphertexts, there are T^k valid input combinations that the TM may execute on. However, when the TM is executing on any input combination, we must ensure that it cannot mix and match symbol, state pairs across different input combinations. Moreover, an encryption for a symbol, state pair produced by some machine M_i should not be decryptable by any machine M_j for $j \neq i$. These issues are handled by careful design of the aggregate functionality to ensure that an execution thread of any input combination by any machine is separate from any other. The proof extends naturally from the single input case. Please see Section 4 for details.

Distributional Indistinguishability. As discussed above, our constructions rely on the security notion of *distributional indistinguishability* (DI) for functional encryption for circuits [31]. Intuitively, this notion says that if the outputs produced by a circuit on two input distributions are merely indistinguishable (as against exactly equal), then the ciphertexts encoding those inputs must also be indistinguishable. In the full version [1] we give a construction of DI secure single input FE from standard FE.

Indistinguishability Obfuscation. Constructing iO for TMs given miFE for TM is straightforward, and adapts the miFE to iO circuit compiler by [33] to the TM setting. As in the circuit case, an miFE for TM that supports two ciphertext queries and single key query suffices for this transformation. Please see Section 5 for details. Since our security proof for miFE for TM is tight, this compiler yields iO for TM from sub-exponentially secure FE for circuits rather than sub-exponentially secure iO for circuits.

Organization of the paper. Definitions and preliminaries are provided in Section 2 as well as the full version [1]. In Section 3, we provide our construction for single input FE for Turing machines. In Section 4, we provide our construction for multi-input FE for Turing machines for any fixed arity k and in Section 5 we describe our iO for TMs for bounded inputs.

2 Preliminaries

In this section, we define some notation and preliminaries that we require.

Notation. We begin by defining the notation that we will use throughout the paper. We use bold letters to denote vectors and the notation $[a, b]$ to denote the set of integers $\{k \in \mathbb{N} \mid a \leq k \leq b\}$. We use $[n]$ to denote the set $[1, n]$. Concatenation is denoted by the symbol \parallel .

We say a function $f(n)$ is *negligible* if it is $O(n^{-c})$ for all $c > 0$, and we use $\text{negl}(n)$ to denote a negligible function of n . We say $f(n)$ is *polynomial* if it is $O(n^c)$ for some $c > 0$, and we use $\text{poly}(n)$ to denote a polynomial function of n . We use the abbreviation PPT for probabilistic polynomial-time. We say an event occurs with *overwhelming probability* if its probability is $1 - \text{negl}(n)$. The function $\log x$ is the base 2 logarithm of x .

2.1 Definitions: FE for Circuits

In this section, we define functional encryption for circuits, in both the single and multi-input setting.

Single Input Functional Encryption for Circuits Let $\mathcal{X} = \{\mathcal{X}_\lambda\}_{\lambda \in \mathbb{N}}$ and $\mathcal{Y} = \{\mathcal{Y}_\lambda\}_{\lambda \in \mathbb{N}}$ denote ensembles where each \mathcal{X}_λ and \mathcal{Y}_λ is a finite set. Let $\mathcal{F} = \{\mathcal{F}_\lambda\}_{\lambda \in \mathbb{N}}$ denote an ensemble where each \mathcal{F}_λ is a finite collection of circuits, and each circuit $f \in \mathcal{F}_\lambda$ takes as input a string $\mathbf{x} \in \mathcal{X}_\lambda$ and outputs $f(\mathbf{x}) \in \mathcal{Y}_\lambda$.

A functional encryption scheme CktFE for \mathcal{F} consists of four algorithms $\text{CktFE} = (\text{CktFE.Setup}, \text{CktFE.Keygen}, \text{CktFE.Enc}, \text{CktFE.Dec})$ defined as follows.

- $\text{CktFE.Setup}(1^\lambda)$ is a PPT algorithm that takes as input the unary representation of the security parameter and outputs the master public and secret keys (PK, MSK). Sometimes, the CktFE.Setup algorithm may also accept as input a parameter 1^ℓ , denoting the length of the input. In this case, the input lives in domain \mathcal{X}^ℓ .
- $\text{CktFE.Keygen}(\text{MSK}, f)$ is a PPT algorithm that takes as input the master secret key MSK and a circuit $f \in \mathcal{F}_\lambda$ and outputs a corresponding secret key SK_f .
- $\text{CktFE.Enc}(\text{PK}, \mathbf{x})$ is a PPT algorithm that takes as input the master public key PK and an input message $\mathbf{x} \in \mathcal{X}_\lambda$ and outputs a ciphertext CT.
- $\text{CktFE.Dec}(\text{SK}_f, \text{CT}_\mathbf{x})$ is an (a deterministic) algorithm that takes as input the secret key SK_f and a ciphertext $\text{CT}_\mathbf{x}$ and outputs $f(\mathbf{x})$.

Definition 1 (Correctness). A functional encryption scheme CktFE is correct if for all $\lambda \in \mathbb{N}$, all $f \in \mathcal{F}_\lambda$ and all $x \in \mathcal{X}_\lambda$,

$$\Pr \left[(\text{PK}, \text{MSK}) \leftarrow \text{CktFE.Setup}(1^\lambda); \right. \\ \left. \text{CktFE.Dec}(\text{CktFE.Keygen}(\text{MSK}, f), \text{CktFE.Enc}(\text{PK}, \mathbf{x})) \neq f(\mathbf{x}) \right] = \text{negl}(\lambda)$$

where the probability is taken over the coins of CktFE.Setup , CktFE.Keygen , and CktFE.Enc .

Definition 2 (Compactness [5]). A functional encryption scheme for circuits is said to be compact if for any input message \mathbf{x} , the running time of the encryption algorithm is polynomial in the security parameter and the size of \mathbf{x} . In particular, it does not depend on the circuit description size or the output length of any function f supported by the scheme.

A weaker version of compactness, known as **succinct** or semi-compact FE, allows the run time of the encryption algorithm to depend on the output length of the functions. Equivalently, a semi-compact FE scheme is simply a compact FE scheme when we restrict our attention to functions with single-bit outputs.

Distributional Indistinguishability for Circuit FE. In this section we define the notion of distributional indistinguishability for functional encryption for circuits. The notion was first defined by [31, Sec 3.4] in the context of reusable garbled circuits, i.e. single key functional encryption but may be generalized to the multi-key setting in a straightforward way. Intuitively, this notion says that if the outputs produced by a circuit on two input distributions are indistinguishable, then the ciphertexts encoding those inputs must also be indistinguishable.

Definition 3. A functional encryption scheme \mathcal{F} for a circuit family \mathcal{G} is secure in the distributional indistinguishability game, if for all PPT adversaries \mathcal{A} , the advantage of \mathcal{A} in the following experiment is negligible in the security parameter λ :

1. *Public Key:* Challenger returns PK to the adversary.
2. *Pre-Challenge Key Queries:* \mathcal{A} may adaptively request keys for any circuits $g_i \in \mathcal{G}$. In response, \mathcal{A} is given the corresponding keys SK_{g_i} . This step may be repeated any polynomial number of times by the attacker.
3. *Challenge Declaration:* $\mathcal{A}(1^\lambda, \text{PK})$ outputs two ensembles of challenge distributions $(D_0(\lambda), D_1(\lambda))$ ⁶ to the challenger, subject to the restriction that for any $\mathbf{x}_0 \leftarrow D_0, \mathbf{x}_1 \leftarrow D_1$, it holds that $g_i(\mathbf{x}_0) \stackrel{c}{\approx} g_i(\mathbf{x}_1)$ for all i .
4. *Challenge CT:* \mathcal{A} requests the challenge ciphertext, to which challenger chooses a random bit b , samples $\mathbf{x}_b \leftarrow D_b$ and returns the ciphertext $\text{CT}_{\mathbf{x}_b}$.
5. *Key Queries:* The adversary may continue to request keys for additional functions g_i , subject to the same restriction that for any $\mathbf{x}_0 \leftarrow D_0, \mathbf{x}_1 \leftarrow D_1$, it holds that $g_i(\mathbf{x}_0) \stackrel{c}{\approx} g_i(\mathbf{x}_1)$ for all i .
6. \mathcal{A} outputs a bit b' , and succeeds if $b' = b$.

The advantage of \mathcal{A} is the absolute value of the difference between its success probability and $1/2$. In the selective game, the adversary is required to declare the challenge distributions in the very first step, without seeing the public key.

Comparison with Standard Indistinguishability. We note that the standard indistinguishability game is implied by the above by restricting the adversary to choose distributions D_0, D_1 above to simply be two messages $\mathbf{x}_0, \mathbf{x}_1$ with probability 1 and requesting keys that satisfy $g_i(\mathbf{x}_0) = g_i(\mathbf{x}_1)$ for all i , which is a special case of $g_i(\mathbf{x}_0) \stackrel{c}{\approx} g_i(\mathbf{x}_1)$.

Decomposable functional encryption for circuits. In this section, we recall the notion of *decomposable functional encryption* (DFE) defined by [2]. Decomposable functional encryption is analogous to the notion of decomposable randomized encodings [9]. Intuitively, decomposability requires that the public key PK and the ciphertext $\text{CT}_{\mathbf{x}}$ of a functional encryption scheme be decomposable into components PK_i and CT_i for $i \in [|\mathbf{x}|]$, where CT_i depends on a single deterministic bit x_i and the public key component PK_i . In addition, the ciphertext may contain components that are independent of the message and depend only on the randomness.

Formally, let $\mathbf{x} \in \{0, 1\}^k$. A functional encryption scheme is said to be decomposable if there exists a deterministic function $\mathcal{E} : \mathcal{P} \times \{0, 1\} \times \mathcal{R}_1 \times \mathcal{R}_2 \rightarrow \mathcal{C}$ such that:

1. The public key may be interpreted as $\text{PK} = (\text{PK}_1, \dots, \text{PK}_k, \text{PK}_{\text{indpt}})$ where $\text{PK}_i \in \mathcal{P}$ for $i \in [k]$. The component $\text{PK}_{\text{indpt}} \in \mathcal{P}^j$ for some $j \in \mathbb{N}$.
2. The ciphertext may be interpreted as $\text{CT}_{\mathbf{x}} = (\text{CT}_1, \dots, \text{CT}_k, \text{CT}_{\text{indpt}})$, where

$$\text{CT}_i = \mathcal{E}(\text{PK}_i, x_i, r, \hat{r}_i) \quad \forall i \in [k] \quad \text{and} \quad \text{CT}_{\text{indpt}} = \mathcal{E}(\text{PK}_{\text{indpt}}, r, \hat{r})$$

Here $r \in \mathcal{R}_1$ is common randomness used by all components of the encryption. Apart from the common randomness r , each CT_i may additionally make use of independent randomness $\hat{r}_i \in \mathcal{R}_2$.

⁶ We omit the parameter λ in what follows for brevity of notation.

We note that if a scheme is decomposable “bit by bit”, i.e. into k components for inputs of size k , it is also decomposable into components corresponding to any partition of the interval $[k]$. Thus, we may decompose the public key and ciphertext into any $i \leq k$ components of length k_i each, such that $\sum k_i = k$. We will sometimes use $\bar{\mathcal{E}}(\mathbf{y})$ to denote the tuple of function values obtained by applying \mathcal{E} to each component of a vector, i.e. $\bar{\mathcal{E}}(\text{PK}, \mathbf{y}, r) \triangleq \left(\mathcal{E}(\text{PK}_1, y_1, r, \hat{r}_1), \dots, \mathcal{E}(\text{PK}_k, y_k, r, \hat{r}_k) \right)$, where $|\mathbf{y}| = k$. We assume that given the security parameter, the spaces \mathcal{P} , \mathcal{R}_1 , \mathcal{R}_2 , \mathcal{C} are fixed, and the length of the message $|\mathbf{x}|$ can be any polynomial.

Multi-Input Functional Encryption for Circuits. We define the notion of private-key t -input functional encryption for circuits here. Our definition follows that of [40].

Let $\forall i \in [t], \mathcal{X}_i = \{(\mathcal{X}_i)\}_{\lambda \in \mathbb{N}}$ and $\mathcal{Y} = \{\mathcal{Y}_\lambda\}_{\lambda \in \mathbb{N}}$ be ensembles of finite sets, and let $\mathcal{F} = \{\mathcal{F}_\lambda\}_{\lambda \in \mathbb{N}}$ be an ensemble of finite t -ary function families. For each $\lambda \in \mathbb{N}$, each function $f \in \mathcal{F}_\lambda$ takes as input t strings, $\mathbf{x}_1 \in (\mathcal{X}_1)_\lambda, \dots, \mathbf{x}_t \in (\mathcal{X}_t)_\lambda$, and outputs a value $f(\mathbf{x}_1, \dots, \mathbf{x}_t) \in \mathcal{Y}_\lambda$.

A private-key t -input functional encryption scheme t -CktFE for \mathcal{F} consists of four algorithms t -CktFE = $(t$ -CktFE.Setup, t -CktFE.Keygen, t -CktFE.Enc, t -CktFE.Dec) defined as follows.

- t -CktFE.Setup(1^λ) is a PPT algorithm that takes as input the unary representation of the security parameter and outputs the master secret key MSK.
- t -CktFE.Keygen(MSK, f) is a PPT algorithm that takes as input the master secret key MSK and a circuit $f \in \mathcal{F}_\lambda$ and outputs a corresponding secret key SK_f .
- t -CktFE.Enc(MSK, \mathbf{m} , ind) is a PPT algorithm that takes as input the master secret key MSK, an input message $\mathbf{m} = \mathbf{x}_i \in (\mathcal{X}_i)_\lambda$ if $\text{ind} = i, i \in [t]$, and outputs a ciphertext CT_{ind} .
- t -CktFE.Dec($\text{SK}_f, (\text{CT}_1, \dots, \text{CT}_t)$) is an (a deterministic) algorithm that takes as input the secret key SK_f and t ciphertexts $\text{CT}_1, \dots, \text{CT}_t$ and outputs a string $y \in \mathcal{Y}_\lambda \cup \perp$.

Definition 4 (Correctness). A private-key t -input functional encryption scheme t -CktFE is correct if for all $\lambda \in \mathbb{N}$, $f \in \mathcal{F}_\lambda$ and all $(\mathbf{x}_1, \dots, \mathbf{x}_t) \in (\mathcal{X}_1)_\lambda \times \dots \times (\mathcal{X}_t)_\lambda$,

$$\Pr \left[\begin{array}{c} t\text{-CktFE.Dec} \left(t\text{-CktFE.Keygen}(\text{MSK}, f), (t\text{-CktFE.Enc}(\text{MSK}, \mathbf{x}_1, 1), \dots, \right. \\ \left. t\text{-CktFE.Enc}(\text{MSK}, \mathbf{x}_t, t)) \right) \neq f(\mathbf{x}_1, \dots, \mathbf{x}_t) \end{array} \right] = \text{negl}(\lambda)$$

Here, $\text{MSK} \leftarrow t\text{-CktFE.Setup}(1^\lambda)$ and probability is taken over the random coins of $t\text{-CktFE.Setup}$, $t\text{-CktFE.Enc}$ and $t\text{-CktFE.Keygen}$.

Distributional Indistinguishability. We define the notion of distributional indistinguishability for a t -input functional encryption scheme for circuits. To begin, we describe a valid t -input adversary.

Definition 5 (Valid t -Input Adversary). A PPT algorithm \mathcal{A} is a valid t -input adversary if for all private-key t -input functional encryption schemes over message space $(\mathcal{X}_1)_\lambda \times \dots \times (\mathcal{X}_t)_\lambda$, and a circuit space \mathcal{F} , for any (f_0, f_1) queried by the adversary, and any t pairs of input distribution ensembles $(D_{01}(\lambda), D_{11}(\lambda)), \dots, (D_{0t}(\lambda), D_{1t}(\lambda))$ ⁷ output by the adversary such that D_{bj} is a distribution over \mathcal{X}_j for $b \in \{0, 1\}, j \in [t]$, it holds that

$$f_0(\mathbf{x}_{01}, \dots, \mathbf{x}_{0t}) \stackrel{c}{\approx} f_1(\mathbf{x}_{11}, \dots, \mathbf{x}_{1t}),$$

where $\mathbf{x}_{bj} \leftarrow D_{bj}$ for $b \in \{0, 1\}, j \in [t]$.

We define the following game between a challenger and an adversary:

⁷ We omit the argument λ where it is implicit for notational brevity.

1. **Key Queries.** \mathcal{A} may adaptively submit key requests for pairs of functions $(f_0, f_1) \in \mathcal{F}$. In response, \mathcal{A} is given the corresponding keys SK_{f_b} for some random bit b chosen by the challenger. This step may be repeated any polynomial number of times by the attacker.
2. **Ciphertext Queries.** $\mathcal{A}(1^\lambda)$ submits ciphertext requests for pairs of challenge distribution ensembles $(D_{01}, D_{11}), \dots, (D_{0t}, D_{1t})$ to the challenger. The challenger samples $\mathbf{x}_j \leftarrow D_{bj}$ for $j \in [t]$ and returns $t\text{-CktFE.Enc}(\text{MSK}, \mathbf{x}_j, j), \forall j \in [t]$. This step may be repeated any polynomial number of times by the attacker.
3. **Guess.** \mathcal{A} outputs a bit b' , and succeeds if $b' = b$.

In the above definition, ciphertext and key queries may be interspersed in any order. The *advantage* of \mathcal{A} is the absolute value of the difference between its success probability and $1/2$. In the *selective* game, the adversary is required to declare the challenge ciphertext distributions in the very first step, without seeing the public key.

Definition 6. A t -input functional encryption scheme $t\text{-CktFE}$ for a circuit family \mathcal{F} is secure in the *distributional indistinguishability game*, if for all valid PPT adversaries \mathcal{A} , the advantage of \mathcal{A} in the above game is negligible in the security parameter λ .

We note that the standard indistinguishability game is the special case where the adversary submits challenge messages rather than distributions and all queried functions must output exactly the same rather than indistinguishable values.

2.2 Definitions: FE for Turing Machines

In this section, we will define functional encryption for Turing Machines (TM). We denote the runtime (i.e. number of steps the head takes) by $\text{runtime}(M, \mathbf{w})$.

Let $\mathcal{M} = \{\mathcal{M}_\lambda\}_{\lambda \in \mathbb{N}}$ be a family of Turing machines with alphabet $\Sigma = \{\Sigma_\lambda\}_{\lambda \in \mathbb{N}}$ and the running time upper-bounded by a polynomial in λ . A functional encryption scheme TMFE for a Turing machine family \mathcal{M} consists of four algorithms $\text{TMFE} = (\text{TMFE.Setup}, \text{TMFE.KeyGen}, \text{TMFE.Enc}, \text{TMFE.Dec})$ defined as follows.

- $\text{TMFE.Setup}(1^\lambda)$ is a PPT algorithm that takes as input the unary representation of the security parameter and outputs the master public and secret keys (PK, MSK) .
- $\text{TMFE.KeyGen}(\text{MSK}, M)$ is a PPT algorithm that takes as input the master secret key MSK and a TM M and outputs a corresponding secret key SK_M .
- $\text{TMFE.Enc}(\text{PK}, \mathbf{x})$ is a PPT algorithm that takes as input the master public key PK , and an input message $\mathbf{x} \in \Sigma_\lambda^*$ of arbitrary length, outputs a ciphertext $\text{CT}_\mathbf{x}$.
- $\text{TMFE.Dec}(\text{SK}_M, \text{CT}_\mathbf{x})$ is an (a deterministic) algorithm that takes as input the secret key SK_M and a ciphertext $\text{CT}_\mathbf{x}$ and outputs a bit b .

Correctness is defined analogously to the circuit setting.

Efficiency [7]. The efficiency property of a public-key FE scheme for Turing machines says that the algorithm TMFE.Setup on input 1^λ should run in time polynomial in λ , TMFE.KeyGen on input the Turing machine M and the master key MSK should run in time polynomial in $(\lambda, |M|)$, TMFE.Enc on input a message \mathbf{x} and the public key should run in time polynomial in $(\lambda, |\mathbf{x}|)$. Finally, TMFE.Dec on input a functional key of M and an encryption of \mathbf{x} should run in time polynomial in $(\lambda, |M|, |\mathbf{x}|, \text{runtime}(M, \mathbf{x}))$.

The multi-input case may be defined as in the circuit setting.

Indistinguishability Obfuscation for Turing Machines As in prior work, we construct iO for Turing machines (TMs) in the setting where the input length is fixed a-priori. A uniform P.P.T machine iO is an indistinguishability obfuscator for a class of Turing machines $\{\mathcal{M}_\lambda\}_{\lambda \in \mathbb{N}}$ with input length L , if the following conditions are satisfied:

1. **Correctness.** For all security parameters $\lambda \in \mathbb{N}$, for any $M \in \mathcal{M}_\lambda$ and every input $\mathbf{x} \in \{0, 1\}^{\leq L}$, we have that:

$$\Pr [M' \leftarrow iO(1^\lambda, M, L) : M'(\mathbf{x}) = M(\mathbf{x})] = 1$$

where the probability is taken over the coin-tosses of the obfuscator iO .

2. **Indistinguishability of Equivalent TMs.** For every ensemble of pairs of Turing machines $\{M_{0,\lambda}, M_{1,\lambda}\}_{\lambda \in \mathbb{N}}$, such that $M_{0,\lambda}(\mathbf{x}) = M_{1,\lambda}(\mathbf{x})$ for every $\mathbf{x} \in \{0, 1\}^{\leq L}$ and $\text{runtime}(M_{0,\lambda}, \mathbf{x}) = \text{runtime}(M_{1,\lambda}, \mathbf{x})$, we have that the following ensembles of pairs of distributions are indistinguishable to any PPT Adv:

$$\left\{ M_{0,\lambda}, M_{1,\lambda}, iO(1^\lambda, M_{0,\lambda}) \right\} \stackrel{c}{\approx} \left\{ M_{0,\lambda}, M_{1,\lambda}, iO(1^\lambda, M_{1,\lambda}) \right\}$$

3. **Succinctness.** For all security parameters $\lambda \in \mathbb{N}$, for any $M \in \mathcal{M}_\lambda$, we have that the running time of $iO(1^\lambda, M, L)$ is $\text{poly}(\lambda, |M|, L)$ and the evaluation time of $iO(M)$ on input \mathbf{x} where $\mathbf{x} \in \{0, 1\}^{\leq L}$, is $\text{poly}(|M|, L, t)$ where $t = \text{runtime}(M, \mathbf{x})$.

2.3 Constrained Pseudorandom Functions

Constrained pseudorandom functions (introduced concurrently by Boneh and Waters (CCS 2013), Boyle, Goldwasser, and Ivan (PKC 2014), and Kiayias, Papadopoulos, Triandopoulos, and Zacharias (CCS 2013)), are pseudorandom functions (PRFs) that allow the owner of the secret key K to compute a constrained key K_f , such that anyone who possesses K_f can compute the output of the PRF on any input x such that $f(x) = 1$ for some predicate f . The security requirement of constrained PRFs state that the PRF output must still look indistinguishable from random for any x such that $f(x) = 0$. We will also require the property of delegatability, formalized below.

Definition 7. [17] Let $F : \{0, 1\}^{\text{seed}(\lambda)} \times \{0, 1\}^{\text{in}(\lambda)} \rightarrow \{0, 1\}^{\text{out}(\lambda)}$ be an efficient function, where seed , in and out are all polynomials in the security parameter λ . We say that F is a delegatable constrained pseudorandom function with respect to a set system $\mathcal{S} \subseteq 2^{\{0, 1\}^{\text{in}(\lambda)}}$ if there exist algorithms (Setup, Constrain, Eval, KeyDel) that satisfy the following:

- Setup($1^\lambda, 1^{\text{in}(\lambda)}$) outputs a pair of keys pk, sk .
- Constrain(sk, S) outputs a constrained key K_S which enables evaluation of $F(\text{sk}, \mathbf{x})$ on all $\mathbf{x} \in S$ and no other \mathbf{x} .
- KeyDel(K_S, S') outputs a constrained key $K_{S \cap S'}$ which enables the evaluation of $F(\text{sk}, \mathbf{x})$ for all $\mathbf{x} \in S \cap S'$ and no other \mathbf{x} . We note that in systems where KeyDel is supported, the Constrain algorithm above can be expressed as a special case of KeyDel by letting sk correspond to the set of all inputs, i.e. $\text{sk} = K_{\{0, 1\}^{\text{in}(\lambda)}}$.
- Eval(K_S, \mathbf{x}) outputs $F(\text{sk}, \mathbf{x})$ if $\mathbf{x} \in S$, \perp otherwise.

Please refer to the full version [1] for the definition of security.

3 Construction: Single Input FE for Turing Machines

In this section, we construct a single input functional encryption scheme for Turing machines, denoted by TMFE from the following ingredients:

1. Two compact functional encryption schemes for circuits, $1FE_1$ and $1FE_2$. We will assume that the scheme $1FE_2$ is decomposable as defined in the preliminaries.
2. A symmetric encryption scheme $SKE = (SKE.KeyGen, SKE.Enc, SKE.Dec)$.
3. A delegatable constrained pseudorandom function (cPRF), denoted by F which supports T delegations for the function family $f_t : \{0, 1\}^{2 \cdot \lambda} \rightarrow \{0, 1\}$ defined as follows. Let x, t denote integers whose binary representations are \mathbf{x}, \mathbf{t} of λ bits. Then,

$$f_t(\mathbf{x}||\mathbf{z}) = 1, \text{ if } x \geq t \text{ and } 0 \text{ otherwise}$$

Intuitively, the function is parametrized by a value t and evaluates to 1 if the first half of its input, $x \geq t$. We will denote the constrained PRF key K_{f_t} corresponding to function f_t by K_t for ease of notation. By the delegation property of constrained PRFs, we have that if $t' \geq t$ then $K_{t'}$ can be derived from K_t . In our construction the parameter t will represent the time step in the computation, which means that a PRF key of the current time step can be used to derive PRF keys for future time steps. We will denote a PRF key for this functionality by F . The security proof makes use a punctured version of the above cPRF, please see the full version [1] for details.

3.1 Construction

Below we provide our construction for single input FE for Turing machines.

Notation. Note that since $1FE_2$ is decomposable, there exists an encoding function \mathcal{E} which encodes each bit of the input and since it is compact, the output length of \mathcal{E} is independent of the circuit class supported by $1FE_2$. Thus, by choosing the encoding function first, the CktFE scheme may support a circuit class that outputs its own ciphertext components. We denote by $\bar{\mathcal{E}}$ the encoding function \mathcal{E} applied bitwise to a vector, i.e. $\bar{\mathcal{E}}(\mathbf{w}) = \mathcal{E}(w_1) \dots \mathcal{E}(w_n)$.

TMFE.Setup(1^λ): Upon input the security parameter 1^λ , do the following:

1. Let $(1FE_2.PK, 1FE_2.MSK) \leftarrow 1FE_2.Setup(1^\lambda)$, where $1FE_2$ is a decomposable functional encryption scheme for the circuit family

$$\text{Next} : \left(\left(\{\text{SYM}\} \times \{0, 1\}^{4\lambda} \times \Sigma \times \text{Trap} \right) \times \left(\{\text{ST}\} \times \mathcal{Q} \right) \right) \rightarrow \left(\mathcal{C}^{1FE_2} \right)^2 \cup \{\text{ACC}, \text{REJ}, \perp\}$$

Here, Σ and \mathcal{Q} are the alphabet and state space respectively of the Turing machine family. The tokens SYM and ST are flags denoting a symbol and a state respectively. The set $\{0, 1\}^{4\lambda}$ encodes in order, a random value key-id associated with a TM M , a cPRF key, the current time step in the computation and the length of the input string, each of λ bits. Here, Trap is a data structure of fixed polynomial length which will be used in the proof. Since we do not need it in the construction, we do not discuss it here, please see Figure 6 for its definition. \mathcal{C}^{1FE_2} denotes the ciphertext space of $1FE_2$, and ACC and REJ are bits indicating accepting and a rejecting states of a TM respectively.

2. Let $(1FE_1.PK, 1FE_1.MSK) \leftarrow 1FE_1.Setup(1^\lambda)$, where $1FE_1$ is a compact, public-key CktFE scheme for the circuit family

$$\text{ReRand} : \left(\{0, 1\}^{3\lambda} \times \Sigma \times \text{Trap} \right) \rightarrow \mathcal{C}^{1FE_2} \times \left(\mathcal{C}^{1FE_2} \cup \{\perp\} \right)$$

Again, $\{0, 1\}^{3\lambda}$ encodes in order, a root cPRF key, a time step and the length of the input string respectively, while Σ , Trap and \mathcal{C}^{1FE_2} are as described above.

3. Output $PK = 1FE_1.PK$ and $MSK = (1FE_1.MSK, (1FE_2.PK, 1FE_2.MSK))$.

TMFE.Enc(PK, \mathbf{w}): Upon input the public key PK , and message \mathbf{w} of arbitrary length $\ell = |\mathbf{w}|$, do the following:

1. Sample the root key K_0 for function f_t where $t = 0$ for the cPRF F described above.

2. For $i \in [\ell]$, let $CT_i = 1FE_1.\text{Enc}(\text{PK}, (K_0, i, \ell, w_i, \text{Trap}))$, where Trap is a data structure which is only relevant in the proof. Here, all fields of Trap are set to \perp except a flag $\text{Trap.mode-real} = 1$ which indicates that we are in the real world. Please see Figure 6 for the definition of Trap .
3. Output $CT_w = \{CT_i\}_{i \in [\ell]}$.

$\text{TMFE.KeyGen}(\text{MSK}, M)$: Upon input the master secret key MSK and the description of a Turing machine M , do the following. We will assume, w.l.o.g. that the TM is oblivious (see [1] for a justification) and $q_{\text{st}} \in \mathcal{Q}$ is the start state of M .

1. Sample a random value $\text{salt} \leftarrow \{0, 1\}^\lambda$.
2. Interpret $\text{MSK} = (1FE_1.\text{MSK}, (1FE_2.\text{PK}, 1FE_2.\text{MSK}))$.
3. Let $\text{SK}_{\text{ReRand}} = 1FE_1.\text{KeyGen}(1FE_1.\text{MSK}, \text{ReRand}_{1FE_2.\text{PK}, \text{salt}, q_{\text{st}}, \perp, \perp})$ where Figure 2 defines the circuit $\text{ReRand}_{1FE_2.\text{PK}, \text{salt}, q_{\text{st}}, \perp, \perp}$.

Function $\text{ReRand}_{1FE_2.\text{PK}, \text{salt}, q_{\text{st}}, C_1, C_2}((K_0, i, \ell, w_i, \text{Trap}))$

(a) **Initialization and Choosing Real or Trapdoor mode.**
Initialize an input vector $\text{inp} = (w_i, q_{\text{st}})$. If $\text{Trap.mode-real} = 1$, set $\text{out} = (c_1, c_2)$, where $c_1 = c_2 = \perp$. If $i \neq 1$, set $\text{inp} = (w_i, \perp)$. **Else** invoke $\text{Trap-Mode}_{\text{ReRand}}(\text{Trap}, \text{inp}, \text{salt}, \ell, C_1, C_2, i)$ as described in Figure 3 to obtain $(\text{inp} = (u_1, u_2), \text{out} = (c_1, c_2))$.

(b) **Computing Encrypted Symbols using randomness derived from cPRF.** If $\text{out}.c_1 = \perp$, do the following.

- i. Noting that $i > 0$, derive delegated cPRF key K_i from K_0 as $K_i = \text{F.KeyDel}(K_0, f_i)$. Compute randomness for encryption as $r_i = \text{F.Eval}(K_i, (i|\text{salt}))$.
- ii. Derive delegated cPRF key $K_{i+1} = \text{F.KeyDel}(K_i, f_{i+1})$. Set $\text{key-id} = \text{salt}$.
- iii. Compute the $1FE_2$ ciphertext component encoding $w_i = \text{inp}.u_1$ for time step i as
$$CT_{\text{sym}, i} = \bar{\mathcal{E}}(1FE_2.\text{PK}_1, (\text{SYM}, \text{key-id}, K_{i+1}, i, \ell, w_i, \text{Trap}); r_i)$$
- iv. Set $\text{out}.c_1 = CT_{\text{sym}, i}$.

(c) **Computing Encrypted State for First Time Step.** If $((\text{out}.c_2 = \perp) \wedge (i = 1))$, do the following.

- i. Compute $1FE_2$ ciphertext component to encode the starting state $q_{\text{st}} = \text{inp}.u_2$ as
$$CT_{\text{st}, 1} = \bar{\mathcal{E}}(1FE_2.\text{PK}_2, (\text{ST}, q_{\text{st}}); r_1)$$
- ii. Set $\text{out}.c_2 = CT_{\text{st}, 1}$.

(d) **Output.** If $i = 1$, output $\text{out} = (CT_{\text{sym}, 1}, CT_{\text{st}, 1})$, else output $\text{out} = (CT_{\text{sym}, i}, \perp)$.

Fig. 2. This circuit re-randomizes the ciphertexts provided during encryption to use randomness derived from a cPRF. The seed for the cPRF is specified in the ciphertext and the input is specified by the key. This ensures that each ciphertext, key pair form a unique “thread” of execution.

4. Let $\text{SK}_{\text{Next}} = 1FE_2.\text{KeyGen}(1FE_2.\text{MSK}, \text{Next}_{1FE_2.\text{PK}, \text{salt}, M, \perp, \perp})$ where Figure 4 defines the circuit $\text{Next}_{1FE_2.\text{PK}, \text{salt}, M, \perp, \perp}$.
5. Output $\text{SK}_M = (\text{SK}_{\text{ReRand}}, \text{SK}_{\text{Next}})$.

$\text{TMFE.Dec}(\text{SK}_M, CT_w)$: Upon input secret key SK_M and ciphertext CT_w , do the following:

1. Interpret $\text{SK}_M = (\text{SK}_{\text{ReRand}}, \text{SK}_{\text{Next}})$ and $CT_w = (CT_1, \dots, CT_{|w|})$.
2. For $i \in [|w|]$, do the following:
 - (a) If $i = 1$, invoke $1FE_1.\text{Dec}(\text{SK}_{\text{ReRand}}, CT_1)$ to obtain $(CT_{\text{sym}, 1}, CT_{\text{st}, 1})$.
 - (b) Else, invoke $1FE_1.\text{Dec}(\text{SK}_{\text{ReRand}}, CT_i)$ to obtain $(CT_{\text{sym}, i}, \perp)$.
3. Denote $((CT_{\text{sym}, 1}, CT_{\text{st}, 1}), CT_{\text{sym}, 2}, \dots, CT_{\text{sym}, |w|})$ as the new sequence of ciphertexts obtained under the Next scheme.
4. Let $t = 1$. While the Turing machine does not halt, do:
 - (a) Invoke $1FE_2.\text{Dec}(\text{SK}_{\text{Next}}, (CT_{\text{sym}, t}, CT_{\text{st}, t}))$ to obtain:
 - ACC or REJ. In this case, output “Accept” or “Reject” respectively, and exit the loop.
 - $(CT_{\text{sym}, t'}, CT_{\text{st}, t+1})$.

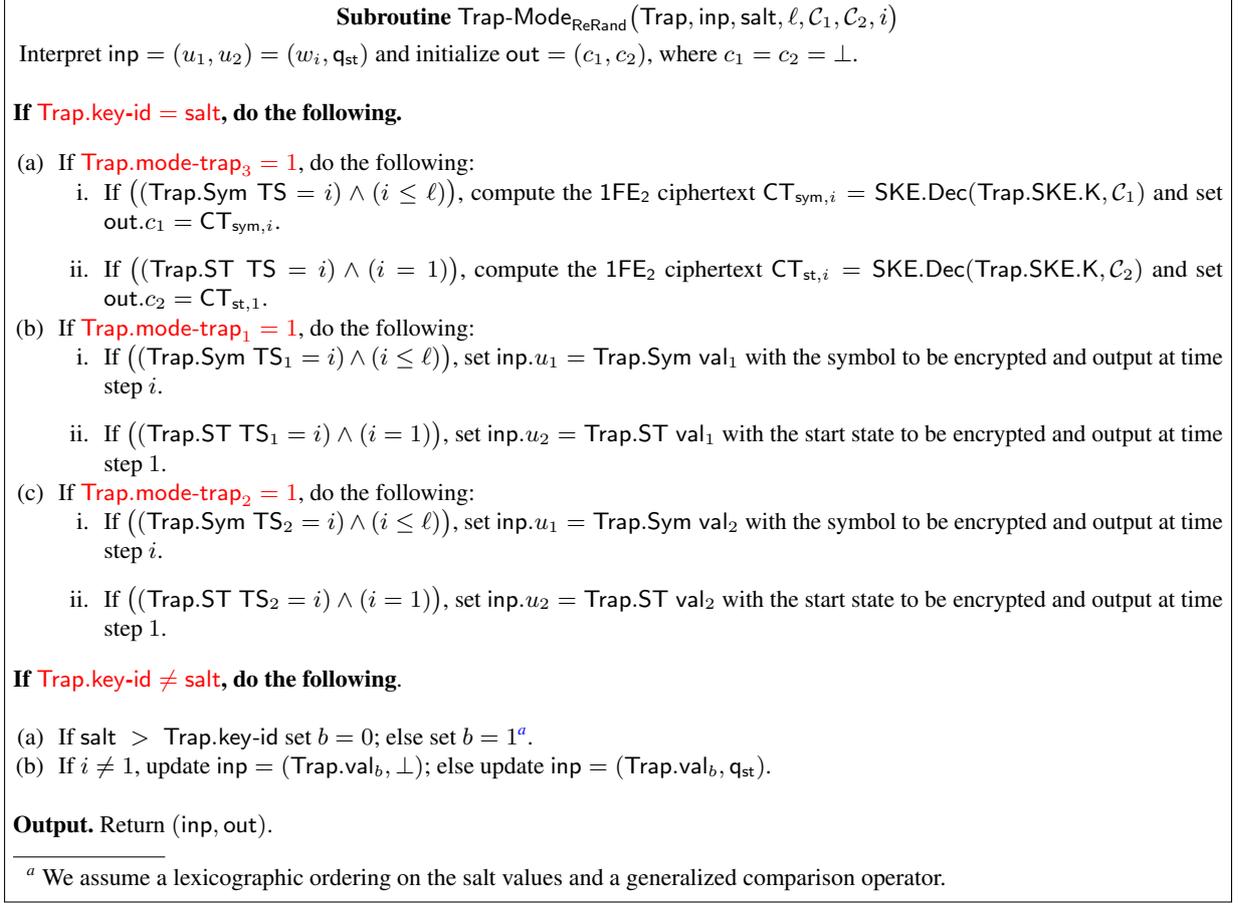


Fig. 3. Subroutine handling the trapdoor modes in ReRand. This is “active” only in the proof.

Note that t' is the next time step that the work tape cell accessed at time step t will be accessed again.

- (b) Let $t = t + 1$ and go to start of loop.

3.2 Correctness and Efficiency of single input TMFE

We now argue that the above scheme is correct. The TMFE.Dec algorithm takes as input a secret key $\text{SK}_M = (\text{SK}_{\text{ReRand}}, \text{SK}_{\text{Next}})$ and a ciphertext $\text{CT}_{\mathbf{w}} = (\text{CT}_1, \dots, \text{CT}_{|\mathbf{w}|})$ under the 1FE_1 scheme supporting the functionality $\text{ReRand} := \text{ReRand}_{1\text{FE}_2.\text{PK}, \text{salt}, q_{\text{st}}, \mathcal{C}_2, \mathcal{C}_2}$. Firstly, note that given a secret key $\text{SK}_{\text{ReRand}}$ along with a ciphertext $\text{CT}_{\mathbf{w}}$, we have as follows.

1. Since CT_1 encodes Trap with $\text{Trap.mode-real} = 1$, hence by the correctness of the 1FE_1 scheme, we get $1\text{FE}_1.\text{Dec}(\text{SK}_{\text{ReRand}}, \text{CT}_1) = (\text{CT}_{\text{sym},1}, \text{CT}_{\text{st},1})$ as output.
2. For $i \in [2, |\mathbf{w}|]$, since CT_i encodes Trap with $\text{Trap.mode-real} = 1$, hence by the correctness of the 1FE_1 scheme, we get $1\text{FE}_1.\text{Dec}(\text{SK}_{\text{ReRand}}, \text{CT}_i) = (\text{CT}_{\text{sym},i}, \perp)$ as the correct output.

The new sequence of 1FE_2 ciphertexts output by ReRand are now sequenced as $((\text{CT}_{\text{sym},1}, \text{CT}_{\text{st},1}), \text{CT}_{\text{sym},2}, \dots, \text{CT}_{\text{sym},|\mathbf{w}|})$. The 1FE_2 scheme supports the functionality $\text{Next} := \text{Next}_{1\text{FE}_2.\text{PK}, \text{salt}, M, \mathcal{C}_1, \mathcal{C}_2}$. Throughout the 1FE_2 decryption, we maintain the invariant that at any time step t , apart from a secret key SK_{Next} , the input to the $1\text{FE}_2.\text{Dec}$ algorithm is an entire 1FE_2 ciphertext decomposed into two

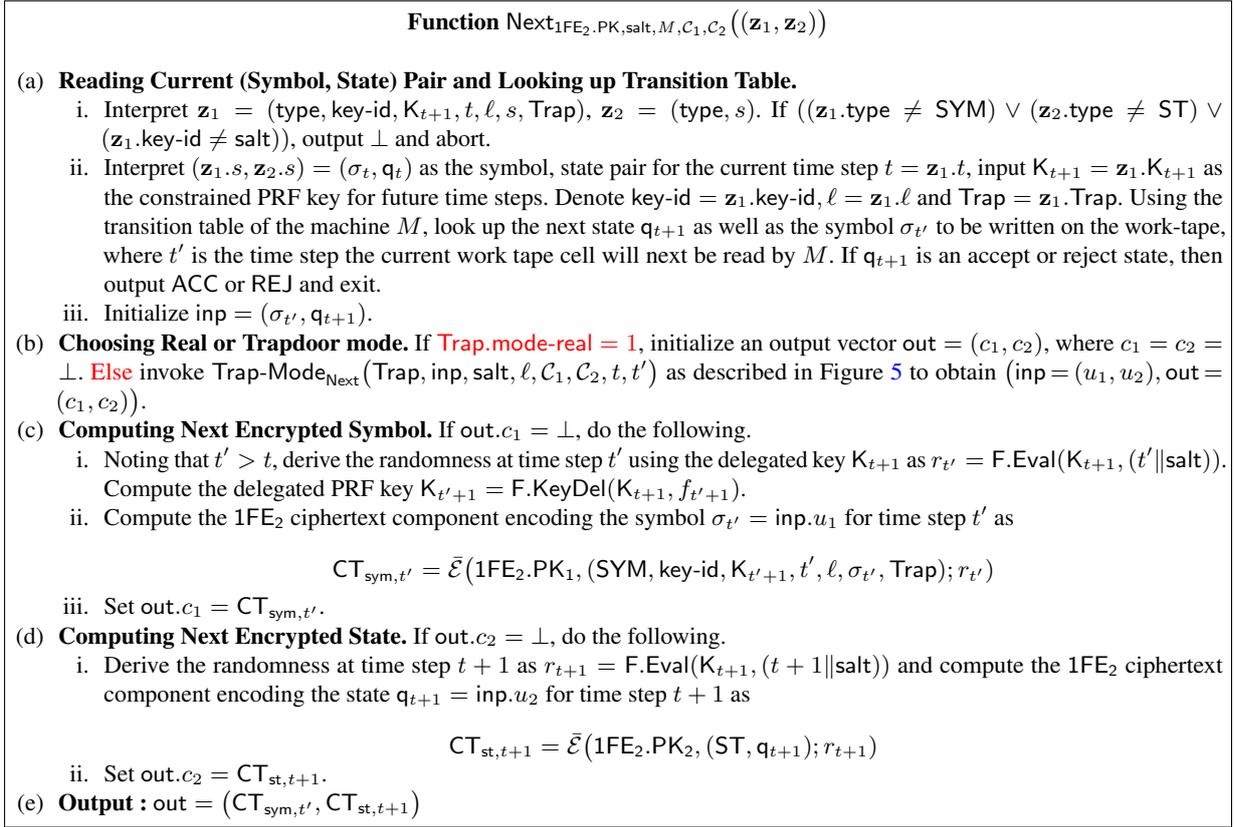


Fig. 4. Function to mimic TM computation. It reads the current symbol, state pair and outputs an encryption of the new state and symbol to be written under the appropriate randomness generated using a cPRF.

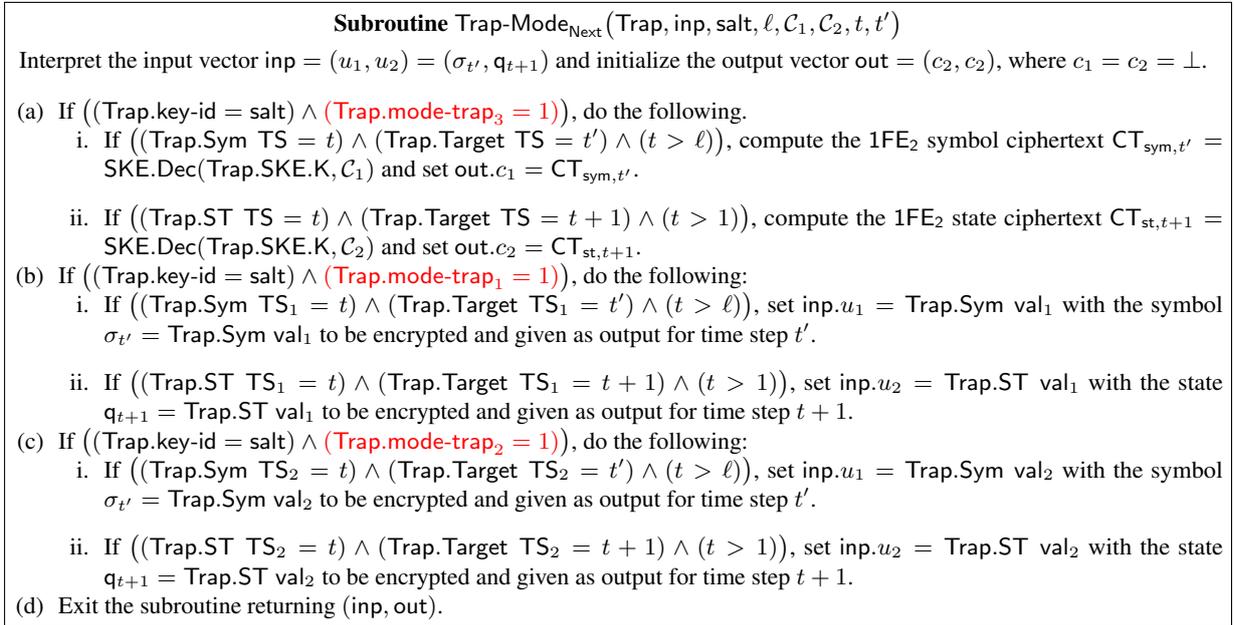


Fig. 5. Subroutine handling the trapdoor modes in Next. This is “active” only in the proof.

components corresponding to a symbol and a state ciphertext both of which are computed with the same randomness, which is computed as $\text{F.Eval}(K_0, (t || \text{salt}))$ ⁸.

⁸ We do not explicitly construct ciphertext components corresponding to blank tape cells in the Next functionality for ease of exposition; we assume w.l.o.g that any non-input cell that is accessed by the OTM has been written to by the Next functionality in a previous step, thus generating the requisite symbol ciphertext.

We show that given a secret key SK_{Next} and the sequence of ciphertexts $((CT_{\text{sym},1}, CT_{\text{st},1}), CT_{\text{sym},2}, \dots, CT_{\text{sym},|\mathbf{w}|})$ generated from the outputs of the $1FE_1.\text{Dec}$ algorithm, $1FE_2.\text{Dec}$ correctly computes the decomposed ciphertext components of a symbol and a state that occur along the computation path and finally outputs the value of machine M on the sequenced input. Define $\tau = \text{runtime}(M, \mathbf{w})$. Formally, by the correctness of $1FE_2$ scheme, at any time step $t \in [\tau - 2]$, $1FE_2.\text{Dec}(SK_{\text{Next}}, (CT_{\text{sym},t}, CT_{\text{st},t}))$ correctly outputs either $(CT_{\text{sym},t'}, CT_{\text{st},t'+1})$ with $t < t' \leq \tau - 1$. Further, for any time step $t \in [\tau - 2]$, we have:

1. Let $t \in [\tau - 2] \setminus [\ell]$. If the current work tape cell was accessed⁹, at some time step $\tilde{t} < t$, then $CT_{\text{sym},t}$ encoding $(\text{SYM}, \text{key-id}, K_{t+1}, t, \ell, \sigma_t, \text{Trap})$ was constructed at time step \tilde{t} . Note that σ_t may be the blank symbol β . When $t \in [\ell]$, $CT_{\text{sym},t}$ is constructed at time step t via the ReRand circuit.
2. The ciphertext component $CT_{\text{st},t}$ encoding (ST, q_t) at time step t was constructed at time step $t - 1$ for $t > 1$ and at time step 1, when $t = 1$.
3. The randomness $r_t = \text{F.Eval}(K_{\tilde{t}+1}, (t \parallel \text{salt})) = \text{F.Eval}(K_t, (t \parallel \text{salt}))$ binds the components $CT_{\text{sym},t}$ and $CT_{\text{st},t}$.

Thus, at any given time step $t \in [\tau - 2]$, we have a complete ciphertext of $1FE_2$ which may be fed again with SK_{Next} to $1FE_2.\text{Dec}$ in order to proceed with the computation. Thus, the execution of $1FE_2.\text{Dec}$ at the $(\tau - 2)^{\text{th}}$ time step provides the complete pair $(CT_{\text{sym},\tau-1}, CT_{\text{st},\tau-1})$. By the correctness of $1FE_2$ scheme again, at time step $t = \tau - 1$, invoking $1FE_2.\text{Dec}(SK_{\text{Next}}, (CT_{\text{sym},\tau-1}, CT_{\text{st},\tau-1}))$ outputs either “Accept” or “Reject” by simulating the execution of M for the final time step τ inside the function Next, thus correctly outputting $M(\mathbf{w})$.

Efficiency. The TMFE construction described above inherits its efficiency from the underlying CktFE constructions. Note that the ciphertext is compact and is of size $\text{poly}(\lambda, |\mathbf{w}|)$. Also, the running time of the decryption procedure is input specific since it mimics the computation of M on \mathbf{w} using secret key encoding M and ciphertext encoding all the intermediate states of the computation. Additionally, the public parameters are short $\text{poly}(\lambda)$, since these are just the public parameters of a compact CktFE scheme. The function keys are also short, since they are CktFE function keys for circuits ReRand and Next which are of size $\text{poly}(\lambda)$ and $\text{poly}(|M|, \lambda)$ respectively.

3.3 Proof of Security for Single Input TMFE

Next, we prove that the above TMFE scheme satisfies distributional indistinguishability (DI) for single (or constant) length outputs, as long as the underlying CktFE scheme satisfies distributional indistinguishability for any output length. In the full version [1], we provide an instantiation of a CktFE scheme satisfying distributional indistinguishability.

Theorem 1. *Assume that the functional encryption schemes for circuits $1FE_1$ and $1FE_2$ are DI secure and that F is a secure cPRF for the function family defined above. Then, the construction of functional encryption for Turing machines TMFE is selective DI secure for single bit outputs.*

The Trapdoor Data Structure. To implement the approach discussed in Section 1, we will make use of a data-structure Trap that lets us store all the requisite trapdoor information needed for the security proof within the ciphertext. In our construction, decryption of a particular input by a particular function key results in a chain of ciphertexts, each of which contain the trapdoor data structure. In the real world, this information is not used but as we progress through the proof, different fields become relevant. The data structure is outlined in Figure 6.

⁹ We assume that every time a cell is accessed, it is written to, by writing the same symbol again if no change is made.

mode-real	key-id	val ₀	val ₁	SKE.K	⊥
mode-trap ₁	Target TS ₁	Sym TS ₁	Sym val ₁	ST TS ₁	ST val ₁
mode-trap ₂	Target TS ₂	Sym TS ₂	Sym val ₂	ST TS ₂	ST val ₂
mode-trap ₃	Target TS	Sym TS	⊥	ST TS	⊥

Fig. 6. Data Structure Trap used for Proof

Row 1. Above, key-id refers to the particular function key being considered and we switch the execution chain from $b = 0$ to $b = 1$ key by key. All the ciphertexts in a given execution chain share the key-id value. We assume a lexicographic order on the key-id fields, this can be easily ensured by having a counter as part of the key-id field. We do not make this explicit below for notational brevity. If key-id* is the key identity programmed in a particular execution chain, then all keys with values smaller than key-id* will decrypt the chain using the input bit $b = 1$, and all keys with values larger than key-id* will use $b = 0$. Hence, the $1FE_1$ ciphertexts provided by the encryptor must encode messages corresponding to both values of b , the fields val₀ and val₁ are designed for this purpose¹⁰. Note that $1FE_2$ ciphertexts computed by decryption need not track messages corresponding to both values of b , since the “chain is extended” via decryption corresponding to exactly one of $b = 0$ or $b = 1$ depending on the relation between the key identities in the ciphertext and the function key. The field SKE.K refers to the key of a symmetric key encryption scheme, which is used to decrypt some encrypted value embedded in the function key. This is a standard trick when the key must hide something in the public key setting. The flag mode-real means the scheme operates in the real world mode and the trapdoor information is not used.

Rows 2 and 3. The fields Target TS₁ and Target TS₂ refer to the time steps corresponding to the “broken link” in the decryption chain, namely the two time steps for which the ciphertext and function key are being programmed so as to switch from $b = 0$ to $b = 1$. The fields Sym TS₁ and ST TS₁ are the time steps when the symbol and state ciphertexts for time step Target TS₁ are generated; for instance ST TS₁ = Target TS₁ - 1 since the state ciphertext for a given time step is always generated in the previous time step, while the symbol ciphertext for a given time step may be generated much earlier. Sym TS₂ and ST TS₂ are defined analogously. The fields Sym val₁ and ST val₁ contain the symbol and state values which will be encrypted in the hybrid at the time steps Sym TS₁ and ST TS₁ when mode-trap₁ is set; Sym val₂ and ST val₂ are defined analogously.

Row 4. When mode-trap₃ is set, the symbol and state values are set to ⊥, and the values hard coded in the function key are used for the target time step. In more detail, the function key contains SKE encryptions of symbol and state ciphertexts corresponding to time step Target TS hard-coded within itself. If key-id* = key-id, where key-id* is the key identity programmed in a particular execution chain and key-id is the key identity of the function key in question, and mode-trap₃ = 1, then at time steps SYM TS and ST TS the SKE secret key in row 1 of the Trap data structure is used to decrypt the SKE encryptions and output the encrypted values.

The Hybrids. We now proceed to describe our hybrids. For simplicity we first describe the hybrids for a single function request, for some Turing machine M . We denote by T the time taken by M to run on the challenge messages. Since the proof is very involved, we describe it first for the weak selective game, where the adversary specifies the challenge vectors and machine at the same time. In the full version [1] we discuss how to remove this restriction.

$\mathcal{H}(0)$: This is the real world, when mode-real = 1 and mode-trap₁ = mode-trap₂ = mode-trap₃ = ⊥.

$\mathcal{H}(1, 1)$: In this world, all ciphertexts (constructed by the encryptor as well as function keys) have mode-real = ⊥, mode-trap₁ = 1, mode-trap₂ = 1, mode-trap₃ = ⊥. We program the last link in

¹⁰ For the knowledgeable reader, this is similar to what was done by [5].

the decryption chain for switching bit b by setting:

$$\text{Target TS}_1 = T - 1, \text{Target TS}_2 = T - 2$$

The fields Sym TS_1 and ST TS_1 contain the time steps when the symbol and state ciphertext pieces are generated for time step $T - 1$, and the fields Sym val_1 and ST val_1 contain the symbol and state values which must be encrypted by the function key in the above time steps when mode-trap_1 is set. Note that these fields exactly mimic the behaviour in the real world, namely the time steps and values are set to be exactly what the real world decryption would output. The fields corresponding to TS_2 are defined analogously.

Indistinguishability follows from security of 1FE_1 , since the decryption values in both hybrids are exactly the same.

$\mathcal{H}(1, 2)$: Hardwire the key with an SKE encryption of symbol and state ciphertexts output at step $T - 1$ for $b = 0$. Use the same ciphertexts as would be generated in the previous hybrid.

Indistinguishability follows from security of SKE, since the only difference is the value of the message encrypted using SKE which is embedded in the key.

$\mathcal{H}(1, 3)$: Set $\text{mode-trap}_1 = \perp$, $\text{mode-trap}_2 = 1$, $\text{mode-trap}_3 = 1$ and $\text{Target TS} = T - 1$. In this hybrid the hardwired value in the key is used to be output as step $T - 1$ ciphertext.

Indistinguishability follows from security of 1FE_1 , since the decryption values in both hybrids are exactly the same.

$\mathcal{H}(1, 4)$: Change normal root key K_0 to punctured root key K_0^{T-1} which punctures all delegated keys at point $(T - 1 \parallel \text{key-id})$.

Indistinguishability follows from security of 1FE_1 . Note that we evaluate the cPRF at point $(T - 1 \parallel \text{key-id})$ only to construct the 1FE_2 ciphertext output at time step $T - 1$ identified with key-id . This ciphertext is currently hardwired in the function key, and is computed exactly the same way in both hybrids. Thus, the cPRF key is only required to compute randomness of points $\neq (T - 1 \parallel \text{key-id})$, for which the punctured key suffices, and which moreover evaluates to the same value as the normal key on all such points. Hence, we have that the decryption values in both hybrids are exactly the same. Note that the punctured key is not used to evaluate on the punctured points.

$\mathcal{H}(1, 5)$: Switch the randomness in the 1FE_2 ciphertexts for time step $T - 1$ which are hardwired in the key to true randomness.

Indistinguishability follows from security of punctured cPRF for the aforementioned function family, since the remainder of the distribution only uses the punctured key.

$\mathcal{H}(1, 6)$: Switch the value encoded in the 1FE_2 ciphertexts for time step $T - 1$ which are hardwired in the key to correspond to $b = 1$.

Indistinguishability follows from security of 1FE_2 . Formally, we do a reduction which plays the security game against the 1FE_2 challenger and simulates the TMFE adversary. The reduction simulates 1FE_1 itself and receives the 1FE_2 public and function keys from the challenger. The only difference between the two hybrids is the 1FE_2 ciphertext for time step $T - 1$ which is embedded in the function key as received from the 1FE_2 challenger.

$\mathcal{H}(1, 7)$: Switch randomness back to PRF randomness in the ciphertext hardwired in key, using the punctured key for all but the hardwired ciphertext.

Indistinguishability follows from security of cPRF as discussed above.

$\mathcal{H}(1, 8)$: Switch the punctured root key to the normal root key.

Indistinguishability follows from security of 1FE_1 as discussed above.

$\mathcal{H}(2, 1)$: Switch ciphertext in slot 1 for target $T - 1$ to be for $b = 1$. Slot 2 remains $b = 0$. Set $\text{mode-trap}_3 = \perp$ and $\text{mode-trap}_1 = \text{mode-trap}_2 = 1$.

Indistinguishability follows from security of 1FE_1 , since the decryption values in both hybrids are exactly the same.

$\mathcal{H}(2, 2)$: Hardwire key with SKE encryption of $1FE_2$ ciphertext for time step $T - 2$ and bit $b = 0$ (same as hybrid (1, 2) but for $T - 2$).

Indistinguishability follows from security of SKE as above.

$\mathcal{H}(2, 3)$: Set $\text{mode-trap}_1 = 1$ with target $T - 1$, $\text{mode-trap}_2 = \perp$, and $\text{mode-trap}_3 = 1$ with target $T - 2$.

Indistinguishability follows from security of $1FE_1$, since the decryption values in both hybrids are exactly the same.

$\mathcal{H}(2, 4)$: Switch normal root key to punctured key at point $(T - 2 || \text{key-id})$.

Indistinguishability follows from security of $1FE_1$ as discussed above.

$\mathcal{H}(2, 5)$: Switch randomness to true in the ciphertext hardwired in key.

Indistinguishability follows from security of cPRF as discussed above.

$\mathcal{H}(2, 6)$: Switch hardwired $1FE_2$ ciphertext for step $T - 2$ to correspond to bit $b = 1$.

Indistinguishability follows from security of $1FE_2$.

$\mathcal{H}(2, 7)$: Switch randomness back to use the PRF in the ciphertext hardwired in key.

Indistinguishability follows from security of cPRF as discussed above.

$\mathcal{H}(2, 8)$: Switch punctured root key to normal root key.

Indistinguishability follows from security of $1FE_1$ as discussed above.

$\mathcal{H}(3, 1)$: Intuitively, we slide the trapdoor left by one step, i.e. change target time-steps to $T - 2$ and $T - 3$ in the ciphertext. Now slot 1 for $T - 2$ corresponds to $b = 1$ and slot 2 for $T - 3$ to $b = 0$. Set $\text{mode-real} = \text{mode-trap}_3 = \perp$ and $\text{mode-trap}_1 = \text{mode-trap}_2 = 1$.

Indistinguishability follows from security of $1FE_1$, since the decryption values in both hybrids are exactly the same. Note that now slot $T - 1$ is redundant, since $T - 2$ ciphertext is already switched to $b = 1$.

Hybrid $\mathcal{H}(3, i)$ will be analogous to $\mathcal{H}(2, i)$ for $i \in [8]$.

As we proceed left in the execution chain one step at a time, we reach step ℓ where $\ell = |\mathbf{w}|$, i.e. time steps for which $1FE_1$ ciphertexts are provided by the encryptor. At this point we will hardwire the ReRand key with symbol ciphertexts for ℓ time steps, one at a time, and the Next key for the state ciphertexts¹¹. Moreover, we must now add an additional hybrid in which the challenge $1FE_1$ ciphertext at position ℓ contains the message bit corresponding to $b = 1$; intuitively, we must switch the bit before we slide the trapdoor since the ciphertext for this position is not generated by decrypting the previous ciphertext. In more detail, in $\mathcal{H}(T - \ell, 8)$, analogously to hybrid (1, 8), the $T - (T - \ell) = \ell^{\text{th}}$ bit hard-wired in the trapdoor is changed to 1. We now add one more hybrid, namely:

$\mathcal{H}(T - \ell, 9)$: In this hybrid, we modify the $1FE_1$ challenge ciphertext in position ℓ as follows: the encoded message is changed corresponding to $b = 1$ and flag $\text{mode-real} = 1$. The other flags $\text{mode-trap}_1 = \text{mode-trap}_2 = \text{mode-trap}_3 = \perp$.

Note that all ciphertexts previous to time step ℓ remain unchanged, and output their corresponding symbol ciphertexts correctly. The Next circuit outputs the state ciphertext for time step ℓ corresponding to bit $b = 1$. The only difference between this hybrid and the previous one is that here we use the real mode to output the symbol ciphertext for $b = 1$ whereas previously we used the trapdoor mode to output the same symbol ciphertext. Hence, decryption values in both hybrids are exactly the same, and indistinguishability follows from security of $1FE_1$.

Finally in $\mathcal{H}(T - 1, 9)$, the entire chain has been replaced to use $b = 1$ and all the challenge $1FE_1$ ciphertexts have encoded messages corresponding to $b = 1$ with $\text{mode-real} = 1$.

$\mathcal{H}(T)$: In this hybrid, all the other fields in the trapdoor data structure, excepting mode-real are disabled and set to \perp . This is the real world with $b = 1$.

Since all the encoded messages use $b = 1$, decryption values are all exactly the same as in $\mathcal{H}(T - 1, 9)$, hence indistinguishability follows from security of $1FE_1$.

The formal reductions are provided in the full version [1].

¹¹ There is an exception at time step 1 when both the symbol ciphertext and the start state ciphertexts are hardwired in the ReRand key

Multiple Keys. We handle multiple keys by repeating the above set of hybrids key by key. Each key carries within it an identifier key-id, and if this is less than the key identifier encoded in the ciphertext, the bit $b = 1$ is used, if it is greater then the bit $b = 0$ is used and if it is equal, then the above sequence of hybrids is performed to switch from $b = 0$ to $b = 1$. To support this, the 1FE₁ ciphertexts provided by the encryptor must encode messages corresponding to both values of b , the fields val_0 and val_1 in the trapdoor data structure of Figure 6 are provided for this purpose. Security follows by a standard hybrid argument as in [5], we defer the formal description to the full version of the paper [1].

3.4 Constructing the cPRF.

In the full version [1], we provide a construction for a cPRF F which supports puncturing and delegation as required; the T cPRFs F_i for $i \in [T]$ may each be constructed similarly. To begin, note that we require the root key of F to be punctured at a point i^* (say). The cPRF construction for punctured PRF [17, 36, 18](which is in turn inherited from the standard PRG based GGM [32]) immediately satisfies this constraint, so we are left with the question of delegation.

Recall that we are required to delegate T times, where T is the (polynomial) runtime of the Turing machine on the encrypted input (please see preliminaries in [1]), and the j^{th} delegated key must support evaluation of points $\{(k||z) : z \in \{0, 1\}^\lambda\}$ for $k \geq j$, *except* when $(k||z) = i^*$. This may be viewed as the j^{th} key being punctured on points $[1, j - 1] \cup i^*$. We show that the GGM based construction for puncturing a single point can be extended to puncturing an interval (plus an extra point). Intuitively, puncturing an interval corresponds to puncturing at most λ internal nodes in the GGM tree. In more detail, we show that regardless of the value of j , it suffices to puncture at most λ points in the GGM tree to achieve puncturing of the entire interval $[1, j - 1]$. Please see the full version [1] for details.

4 Construction: Multi-Input FE for Turing Machines

In this section we construct a multi-input functional encryption scheme for Turing machines. Our construction supports a fixed number of encryptors (say k), who may each encrypt a string \mathbf{w}_i of *unbounded* length. Function keys may be provided for Turing machines, so that given k ciphertexts for \mathbf{w}_i and a function key for TM M , decryption reveals $M(\mathbf{w}_1 || \dots || \mathbf{w}_k)$ and nothing else. We use the following ingredients for our construction:

1. A compact, k -input functional encryption scheme for circuits, kFE and a compact, public-key functional encryption scheme 1FE. As before, we will assume that the scheme 1FE is decomposable as defined in the preliminaries.
2. A symmetric encryption scheme $\text{SKE} = (\text{SKE.KeyGen}, \text{SKE.Enc}, \text{SKE.Dec})$.
3. A delegatable constrained pseudorandom function (cPRF), denoted by F which supports T delegations for the function family $f_t : \{0, 1\}^{(k+2) \cdot \lambda} \rightarrow \{0, 1\}$ defined as follows. Let x, t denote integers whose binary representations are \mathbf{x}, \mathbf{t} of λ bits. Then,

$$f_t(\mathbf{x}||\mathbf{z}) = 1, \text{ if } x \geq t \text{ and } 0 \text{ otherwise}$$

The functionalities supported by kFE and 1FE are called *Agg* and *Next* respectively, described next. *Agg* aggregates the inputs $\mathbf{w}_1, \dots, \mathbf{w}_k$ of all k parties into one long “global” string $(\mathbf{w}_1 || \dots || \mathbf{w}_k)$, encrypted under the scheme 1FE. Since the length of this aggregate string is unbounded, a single invocation of *Agg* produces an encryption of a single symbol in the string, and the function is invoked repeatedly to produce ciphertexts for the entire string. Each ciphertext output by the *Agg* scheme contains a symbol w_i as well as the position of the symbol within the global string. The encryption of the symbols (and the initial state) also contains a *global salt* which *Agg* computes from the random salts provided in

the ciphertexts under the k FE scheme by the individual encryptors. The global salt identifies the particular input combination that is aggregated, and serves as input to the PRF in the Next functionality.

Our k -input CktFE scheme may be either private or public key, and will result in the corresponding notion for k -input TMFE. Since the multi input setting for FE is considered more interesting in the symmetric key setting (see [19] for a discussion), we present our construction in the symmetric key setting – the public key adaptation is straightforward.

We note that ciphertexts output by Agg, which are encryptions of the symbols in the aggregate string under the 1FE scheme, are exactly the same as the output of the ReRand function in the single input scheme of Section 3. Therefore, as before, we may have the functionality Next of the 1FE scheme mimic the computation of the Turing machine on the global string $(\mathbf{w}_1 || \dots || \mathbf{w}_k)$. As in the previous construction, 1FE.Dec accepts as its inputs a ciphertext decomposed into two components encoding the current symbol on the worktape and the current state in the computation, both of which have been encrypted using the same randomness, and outputs a ciphertext component corresponding to the symbol written on the tape, as well as the next state. The global salt in the ciphertext, along with a random nonce chosen by KeyGen are used as input to a cPRF as before, to compute the randomness used to generate ciphertexts. This ensures that the execution of a given machine on a given input combination is maintained separate from any other execution, and thwarts “mix and match” attacks, where, for instance, an attacker may try to combine a state generated at some time step t in one execution with a symbol generated at time step t from a different execution.

If we instantiate the underlying multi-input CktFE by the construction of [40], we may let the arity k be poly-logarithmic in the security parameter. If we instantiate multi-input CktFE by the construction of [33], we may support fixed polynomial arity at the cost of worsening the assumption. Note that [33] rely on iO while [40] rely on compact FE. Note that [10] support unbounded polynomial arity, but from public coin DiO as discussed in Section 1.

4.1 Construction of multi-input TMFE

In the following, we denote a k -input, private-key CktFE scheme by k -CktFE and a decomposable, public key CktFE scheme by 1FE. Since our scheme supports an a-priori fixed number of parties, say k , we assume that every user is pre-assigned an index $\text{ind} \in [k]$.

k TMFE.Setup($1^\lambda, 1^k$): Upon input the security parameter 1^λ and the bound 1^k , do the following:

1. **Choosing the functionality for 1FE.** Let 1FE be a decomposable, public-key CktFE for the following circuit family.

$$\text{Next} : \left((\{\text{SYM}\} \times \{0, 1\}^{(k+4)\lambda} \times \Sigma \times \text{Trap}) \times (\{\text{ST}\} \times \mathcal{Q} \times \{0, 1\}^{k \cdot \lambda}) \right) \rightarrow \left(\mathcal{C}^{1\text{FE}} \right)^2 \cup \{\text{ACC}, \text{REJ}, \perp\}$$

The tokens SYM and ST are flags denoting a symbol and a state respectively of a Turing machine M which has Σ and \mathcal{Q} as the alphabet and state space respectively. The set $\{0, 1\}^{(k+4)\lambda}$ encodes in order, a random value key-id associated with a TM M , a constrained PRF key, the current time step in the computation, the length of the input string, each of λ bits and a string of length $k \cdot \lambda$ bits encoding a random value gsalt. Here, Trap is a data structure of fixed polynomial length which will be used in the proof. Since we do not need it in the construction, we do not discuss it here, please see the full version [1] for its definition. The set $\{0, 1\}^{k \cdot \lambda}$ encodes again a random value gsalt associated with the message component for state. $\mathcal{C}^{1\text{FE}}$ is the ciphertext space of 1FE. ACC and REJ denote tokens when M reaches an accepting state and a rejecting state respectively.

2. **Choosing the functionality for k FE.** Let k FE be a k -CktFE for the following circuit family.

$$\text{Agg} : (\{\text{SYM}, \text{SP}\} \times \{0, 1\}^{4\lambda} \times [k] \times \Sigma \times \text{Trap})^k \rightarrow \mathcal{C}^{1\text{FE}} \times \left(\mathcal{C}^{1\text{FE}} \cup \{\perp\} \right)$$

The special token SP denotes an encryption of the length of an input string corresponding to any user. The set $\{0, 1\}^{4\lambda}$ encodes in order, a constrained PRF key, the time step of the current symbol, the input length and a random salt each of λ bits. Σ , Trap and \mathcal{C}^{1FE} are as described above.

3. Choosing keys for kFE and 1FE.

Let $kFE.MSK \leftarrow kFE.Setup(1^\lambda, 1^k)$, $(1FE.PK, 1FE.MSK) \leftarrow 1FE.Setup(1^\lambda, 1^k)$

4. Output $MSK = (kFE.MSK, (1FE.PK, 1FE.MSK))$.

$kTMFE.Enc(MSK, \mathbf{w}_{ind}, ind)$: Upon input the master key MSK , and message \mathbf{w}_{ind} of arbitrary length ℓ_{ind} and an index $ind \in [k]$, do the following:

1. Interpret the input $MSK = (kFE.MSK, (1FE.PK, 1FE.MSK))$.
2. Let $\mathbf{w}_{ind} = w_1 w_2 \dots w_{\ell_{ind}}$. Sample $salt_{ind} \leftarrow \{0, 1\}^\lambda$.
3. Construct the data structure Trap and set all its fields to \perp except a flag **Trap.mode-real = 1** which indicates that we are in the real world. The data structure Trap is only relevant in the proof. Please see [1] for the definition of Trap.

• Encoding Input String and Its Length

4. If $ind = 1$, do the following:

- (a) Sample a root key for the constrained PRF F as $K_0 \leftarrow F.Setup(1^\lambda)$.
- (b) Construct the input message $len_1 = (SP, K_0, \perp, \ell_1, salt_1, 1, \perp, Trap)$.
- (c) Encrypt ℓ_1 as a special ciphertext $CT_{1,SP} = kFE.Enc(kFE.MSK, len)$.
- (d) For $i \in [\ell_1]$ do the following:
 - i. Construct the input message $\mathbf{y}_{1,i} = (SYM, K_0, i, \ell_1, salt_1, 1, w_i, Trap)$.
 - ii. Compute the ciphertext $CT_{1,SYM,i} = kFE.Enc(kFE.MSK, \mathbf{y}_i)$.

5. If $ind \in [2, k]$, do the following:

- (a) Construct the input message $len_{ind} = (SP, \perp, \perp, \ell_{ind}, salt_{ind}, ind, \perp, Trap)$.
- (b) Encrypt ℓ_{ind} as a special ciphertext $CT_{ind,SP} = kFE.Enc(kFE.MSK, len)$.
- (c) For $i \in [\ell_{ind}]$ do the following:
 - i. Construct the input message $\mathbf{y}_{ind,i} = (SYM, \perp, i, \ell_{ind}, salt_{ind}, ind, w_i, Trap)$.
 - ii. Compute the ciphertext $CT_{ind,SYM,i} = kFE.Enc(kFE.MSK, \mathbf{y}_i)$.

6. Output $CT_{\mathbf{w}_{ind}} = (CT_{ind,SP}, \{CT_{ind,SYM,i}\}_{i \in [\ell_{ind}]})$.

$kTMFE.KeyGen(MSK, M)$: Upon input the master secret key MSK and the description of a Turing machine M , do the following. We will assume, w.l.o.g. that the TM is oblivious (see [1] for a justification) and $q_{st} \in \mathcal{Q}$ is the start state of M .

1. Sample a random value $rand \leftarrow \{0, 1\}^\lambda$.
2. Interpret $MSK = (kFE.MSK, (1FE.PK, 1FE.MSK))$.
3. Let $SK_{Agg} = kFE.KeyGen(kFE.MSK, Agg_{1FE.PK, rand, q_{st}, \perp, \perp})$, where Figure 7 defines the circuit $Agg_{1FE.PK, rand, q_{st}, \perp, \perp}$.
4. Let $SK_{Next} = 1FE.KeyGen(1FE.MSK, Next_{1FE.PK, rand, M, \perp, \perp})$, where Figure 9 defines the circuit $Next_{1FE.PK, rand, M, \perp, \perp}$.
5. Output the secret key as $SK_M = (SK_{Agg}, SK_{Next})$.

$kTMFE.Dec(SK_M, \{CT_{\mathbf{w}_i}\}_{i \in [k]})$: Upon input secret key SK_M and k ciphertexts $CT_{\mathbf{w}_1}, \dots, CT_{\mathbf{w}_k}$, do the following:

1. Interpret the secret key as $SK_M = (SK_{Agg}, SK_{Next})$.
2. Parse $CT_{\mathbf{w}_{ind}} = (CT_{ind,SP}, (CT_{ind,SYM,1}, \dots, CT_{ind,SYM,\ell_{ind}}))$ for all $ind \in [k]$.

Function $\text{Agg}_{1\text{FE.PK,rand,qst},\mathcal{C}_1,\mathcal{C}_2}(\mathbf{x}_1, \mathbf{x}_2, \dots, \mathbf{x}_k)$

- (a) Interpret $\mathbf{x}_i = (\text{type}, K, t, \ell, \text{salt}, \text{ind}, s, \text{Trap})$, for $i \in [k]$ and set a flag $\text{proceed}_1 = \text{proceed}_2 = 0$.
- (b) For all $i, j \in [k]$, if $\mathbf{x}_i.\text{ind} \neq \mathbf{x}_j.\text{ind}$ for $i \neq j$, set $\text{proceed} = 1$. If there exists *exactly* one $i \in [k]$ for which $\mathbf{x}_i.\text{type} = \text{SYM}$ and $\mathbf{x}_j.\text{type} = \text{SP}$, $\forall j \in [k] \setminus \{i\}$ and $\text{proceed}_1 = 1$, set $\text{proceed}_2 = 1$. If $\text{proceed}_2 = 0$, output \perp and abort.
- (c) **Initialization and Choosing Real or Trapdoor mode.**
Let $i \in [k]$ be such that $\mathbf{x}_i.\text{type} = \text{SYM}$. Initialize an input vector $\text{inp} = (\sigma, \text{qst})$, where $\sigma = \mathbf{x}_i.s$. Let $\text{gsalt} = (\mathbf{x}_1.\text{salt} \parallel \mathbf{x}_2.\text{salt} \parallel \dots \parallel \mathbf{x}_k.\text{salt})$ and $\ell = \sum_{i=1}^k \mathbf{x}_i.\ell$ denote the global salt and the aggregate input length respectively. Denote $\text{pos} = \mathbf{x}_i.t$ and do the following:
 - i. **Computing Global Symbol Position :** If $1 < \mathbf{x}_i.\text{ind} \leq k$, compute the new position of the symbol as $\text{pos} = \text{pos} + \sum_{r \in \mathcal{S}} \mathbf{x}_r.\ell$, where the set $\mathcal{S} = \{r \mid \mathbf{x}_r.\text{ind} < \mathbf{x}_i.\text{ind}\} \subset [k]$.
 - ii. If **Trap.mode-real = 1**, set $\text{out} = (c_1, c_2)$, where $c_1 = c_2 = \perp$. If $\text{pos} \neq 1$, set $\text{inp} = (\sigma, \perp)$.
 - iii. **Else** obtain $(\text{inp} = (u_1, u_2), \text{out} = (c_1, c_2)) = \text{Trap-Mode}_{\text{Agg}}(\text{Trap}, \text{inp}, \text{rand}, \text{gsalt}, \ell, \mathcal{C}_1, \mathcal{C}_2, \text{pos})$ as described in Figure 8.
- (d) If $((\text{out}.c_1 = \perp) \vee (\text{out}.c_2 = \perp))$, do the following.
 - i. Let $p \in [k]$ be such that $\mathbf{x}_p.\text{ind} = 1$ and denote $K_0 = \mathbf{x}_p.K$ as the root key for cPRF.
 - ii. Derive the randomness for encryption at time step pos as $r_{\text{pos}} = \text{F.Eval}(K_0, (\text{pos} \parallel \text{rand} \parallel \text{gsalt}))$.
 - iii. **Computing Encrypted Symbols using randomness derived from cPRF.** If $\text{out}.c_1 = \perp$, do the following.
 - Compute the delegated PRF key $K_{\text{pos}+1} = \text{F.KeyDel}(K_0, f_{\text{pos}+1})$. Set $\text{key-id} = \text{rand}$.
 - Compute the 1FE symbol ciphertext encoding $\sigma = \text{inp}.u_1$ as $\text{CT}_{\text{sym},\text{pos}} = \bar{\mathcal{E}}(1\text{FE.PK}_1, \mathbf{y}_1; r_{\text{pos}})$, where $\mathbf{y}_1 = (\text{SYM}, \text{key-id}, K_{\text{pos}+1}, \text{pos}, \ell, \text{gsalt}, \sigma, \text{Trap})$.
 - iv. **Computing Encrypted State for First Time Step.** If $((\text{out}.c_2 = \perp) \wedge (\text{pos} = 1))$, do the following.
 - Compute the 1FE state ciphertext encoding $\text{qst} = \text{inp}.u_2$ as $\text{CT}_{\text{st},1} = \bar{\mathcal{E}}(1\text{FE.PK}_2, \mathbf{y}_2; r_1)$, where $\mathbf{y}_2 = (\text{ST}, \text{qst}, \text{gsalt})$. Set $\text{out}.c_2 = \text{CT}_{\text{st},1}$.
- (e) If $\text{pos} = 1$, output $\text{out} = (\text{CT}_{\text{sym},1}, \text{CT}_{\text{st},1})$. Otherwise, output $\text{out} = (\text{CT}_{\text{sym},\text{pos}}, \perp)$.

Fig. 7. This circuit aggregates and re-randomizes the ciphertexts provided during encryption to use randomness derived from a cPRF. The seed for the cPRF is specified in the ciphertext for first party and the input is specified by the key. This ensures that each ciphertext, key pair form a unique “thread” of execution.

• **Aggregate the ciphertexts of all users.**

3. For $i = 1$ to k , do the following:

(a) For $j = 1$ to ℓ_i , do the following:

- i. If $((i = 1) \wedge (j = 1))$, invoke $\text{kFE.Dec}(\text{SK}_{\text{Agg}}, (\text{CT}_{1,\text{SYM},1}, \{\text{CT}_{n,\text{SP}}\}_{n \in [k] \setminus \{1\}}))$ to obtain $(\text{CT}_{\text{sym},1}, \text{CT}_{\text{st},1})$.
- ii. If $((i = 1) \wedge (j > 1))$, invoke $\text{kFE.Dec}(\text{SK}_{\text{Agg}}, (\text{CT}_{1,\text{SYM},j}, \{\text{CT}_{n,\text{SP}}\}_{n \in [k] \setminus \{1\}}))$ to obtain $(\text{CT}_{\text{sym},j}, \perp)$.
- iii. Else, invoke $\text{kFE.Dec}(\text{SK}_{\text{Agg}}, (\text{CT}_{i,\text{SYM},j}, \{\text{CT}_{n,\text{SP}}\}_{n \in [k] \setminus \{i\}}))$ to obtain $(\text{CT}_{\text{sym},\tilde{L}_i+j}, \perp)$, where $\tilde{L}_i = \sum_{m=1}^{i-1} \ell_m$.

• **Execute the TM on aggregated input.**

4. The aggregated sequence of ciphertexts under the Next scheme, of length $L_k = \sum_{j=1}^k \ell_j$ computed above is expressed as:

$$((\text{CT}_{\text{sym},1}, \text{CT}_{\text{st},1}), \text{CT}_{\text{sym},2}, \dots, \text{CT}_{\text{sym},\ell_1}, \text{CT}_{\text{sym},\ell_1+1}, \dots, \text{CT}_{\text{sym},L_k}).$$

5. Let $t = 1$. While the Turing machine does not halt, do:

(a) Invoke $1\text{FE.Dec}(\text{SK}_{\text{Next}}, (\text{CT}_{\text{sym},t}, \text{CT}_{\text{st},t}))$ to obtain:

- ACC or REJ. In this case, output “Accept” or “Reject” respectively, and exit the loop.
- $(\text{CT}_{\text{sym},t'}, \text{CT}_{\text{st},t+1})$.

Note that t' is the next time step that the work tape cell accessed at time step t will be accessed again.

(b) Let $t = t + 1$ and go to start of loop.

Subroutine $\text{Trap-Mode}_{\text{Agg}}(\text{Trap}, \text{inp}, \text{rand}, \text{gsalt}, \ell, \mathcal{C}_1, \mathcal{C}_2, \text{pos})$

Interpret $\text{inp} = (u_1, u_2) = (w_i, q_{\text{st}})$ and initialize $\text{out} = (c_1, c_2)$, where $c_1 = c_2 = \perp$.

If $\text{Trap.key-id} = \text{rand}$, **do the following**.

(a) If $((\text{Trap.global-salt} = \text{gsalt}) \wedge (\text{Trap.mode-trap}_3 = 1))$, do the following:

- i. If $((\text{Trap.Sym TS} = \text{pos}) \wedge (\text{pos} \leq \ell))$, compute $\text{CT}_{\text{sym, pos}} = \text{SKE.Dec}(\text{Trap.SKE.K}, \mathcal{C}_1)$ and set $\text{out}.c_1 = \text{CT}_{\text{sym, pos}}$.
- ii. If $((\text{Trap.ST TS} = \text{pos}) \wedge (\text{pos} = 1))$, compute $\text{CT}_{\text{st, pos}} = \text{SKE.Dec}(\text{Trap.SKE.K}, \mathcal{C}_2)$ and set $\text{out}.c_2 = \text{CT}_{\text{st, 1}}$.

(b) If $((\text{Trap.global-salt} = \text{gsalt}) \wedge (\text{Trap.mode-trap}_1 = 1))$, do the following:

- i. If $((\text{Trap.Sym TS}_1 = \text{pos}) \wedge (\text{pos} \leq \ell))$, set $\text{inp}.u_1 = \text{Trap.Sym val}_1$ with the symbol to be encrypted and output at time step pos .
- ii. If $((\text{Trap.ST TS}_1 = \text{pos}) \wedge (\text{pos} = 1))$, set $\text{inp}.u_2 = \text{Trap.ST val}_1$ with the start state to be encrypted and output at time step 1.

(c) If $((\text{Trap.global-salt} = \text{gsalt}) \wedge (\text{Trap.mode-trap}_2 = 1))$, do the following:

- i. If $((\text{Trap.Sym TS}_2 = \text{pos}) \wedge (\text{pos} \leq \ell))$, set $\text{inp}.u_1 = \text{Trap.Sym val}_2$ with the symbol to be encrypted and output at time step pos .
- ii. If $((\text{Trap.ST TS}_2 = \text{pos}) \wedge (\text{pos} = 1))$, set $\text{inp}.u_2 = \text{Trap.ST val}_2$ with the start state to be encrypted and output at time step 1.

(d) If $\text{Trap.global-salt} < \text{gsalt}$, set $b = 0$, if $\text{Trap.global-salt} > \text{gsalt}$, set $b = 1$.

- i. If $\text{pos} \neq 1$, update $\text{inp} = (\text{Trap.val}_b, \perp)$; else update $\text{inp} = (\text{Trap.val}_b, q_{\text{st}})$.

If $\text{Trap.key-id} > \text{rand}$, set $b = 1$, **if** $\text{Trap.key-id} < \text{rand}$ set $b = 0$.

(a) If $\text{pos} \neq 1$, update $\text{inp} = (\text{Trap.val}_b, \perp)$; else update $\text{inp} = (\text{Trap.val}_b, q_{\text{st}})$.

Output. Return (inp, out) .

Fig. 8. Subroutine handling the trapdoor modes in Agg. This is “active” only in the proof.

4.2 Correctness of Multi-Input TMFE

The proof of correctness is split into two parts. In the first part we argue that, given as input the secret key SK_{Agg} along with k ciphertexts under the kFE scheme, exactly one of which encodes a symbol and the other $(k - 1)$ encode the individual input lengths, the kFE.Dec algorithm computes a 1FE ciphertext component of the symbol with its updated position in the global string. By repeating this process for all symbols encoded by all users, we obtain a sequence of 1FE ciphertext components, each containing its updated position in the aggregated string. Additionally, each of these ciphertext components contains a global/aggregate salt that is generated from concatenating each individual encryptor’s randomly generated salts. This global salt identifies the particular input combination being aggregated.

Correctness of the second part corresponds to the correct execution of the Turing machine on the aggregate sequence of ciphertexts, and this is exactly the same as in Section 3. As before, we maintain the invariant that at any time step t , the input to the 1FE.Dec algorithm is a complete 1FE ciphertext decomposed into two components corresponding to symbol and state (along with additional auxiliary inputs), both computed with the same randomness $\text{F.Eval}(\text{K}_0, (t \parallel \text{rand} \parallel \text{gsalt}))$.

In more detail, we have the following. *Correctness of Aggregation.* Formally, let there be k users so that k ciphertexts $\{\text{CT}_{\mathbf{w}_{\text{ind}}}\}_{\text{ind} \in [k]}$ are given as input to kTMFE.Dec algorithm. For all $\text{ind} \in [k]$, let ℓ_{ind} be the length of input string of user ind . Each ciphertext $\text{CT}_{\mathbf{w}_{\text{ind}}}$ is a sequence $(\text{CT}_{\text{ind, SP}}, (\text{CT}_{\text{ind, SYM}, 1}, \dots, \text{CT}_{\text{ind, SYM}, \ell_{\text{ind}}}))$ of ciphertexts, where the first component $\text{CT}_{\text{ind, SP}}$ encodes the input string length of user ind and the second component $\{\text{CT}_{\text{ind, SYM}, i}\}_{i \in [\ell_{\text{ind}}]}$ encodes in order the i -th symbol w_i of the actual input string $\mathbf{w}_{\text{ind}} = (w_1, w_2, \dots, w_{\ell_{\text{ind}}})$ of the same user. These ciphertexts are generated under the kFE scheme with the master secret key kFE.MSK which supports a k -input functionality $\text{Agg} := \text{Agg}_{\text{1FE.PK, rand, q}_{\text{st}}, \perp, \perp}$. Therefore, given secret key SK_{Agg} , we have:

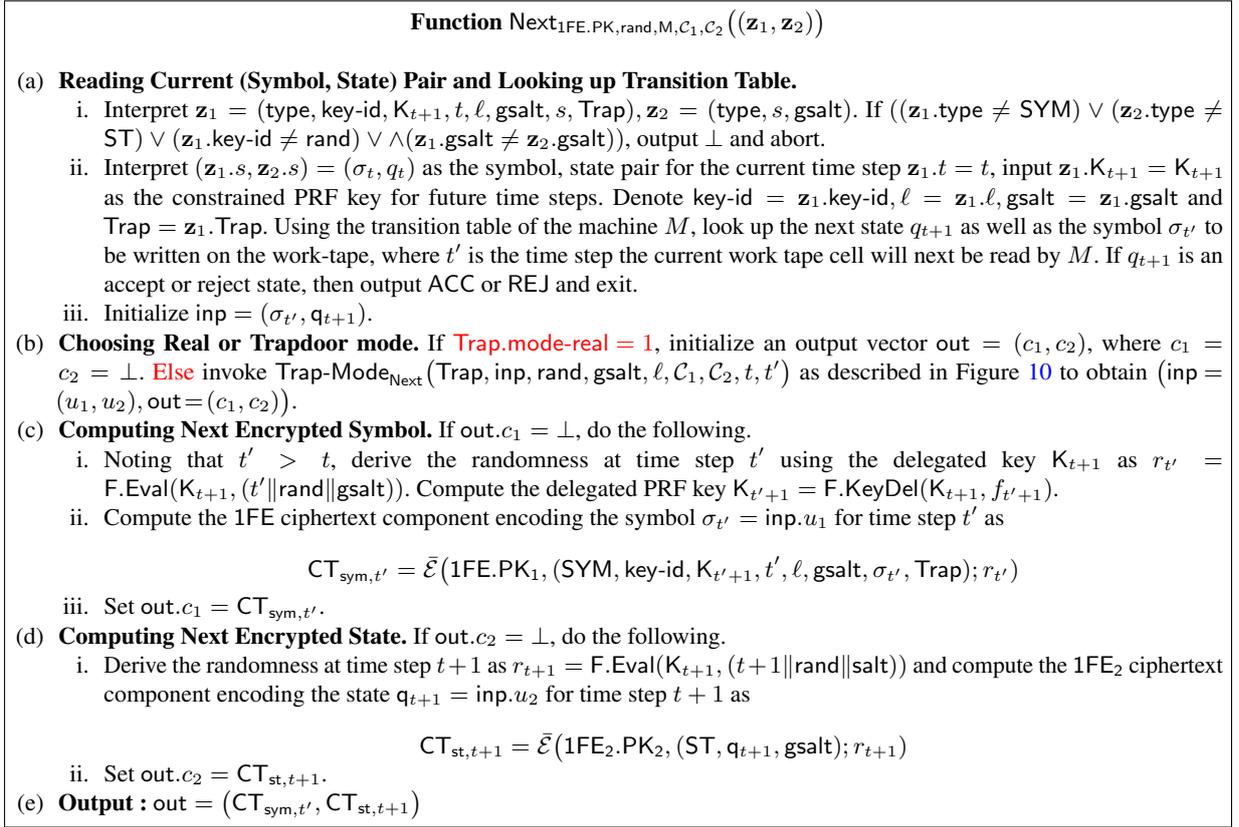


Fig. 9. Function to mimic TM computation. It reads the current symbol, state pair and outputs an encryption of the new state and symbol to be written under the appropriate randomness generated using a cPRF.

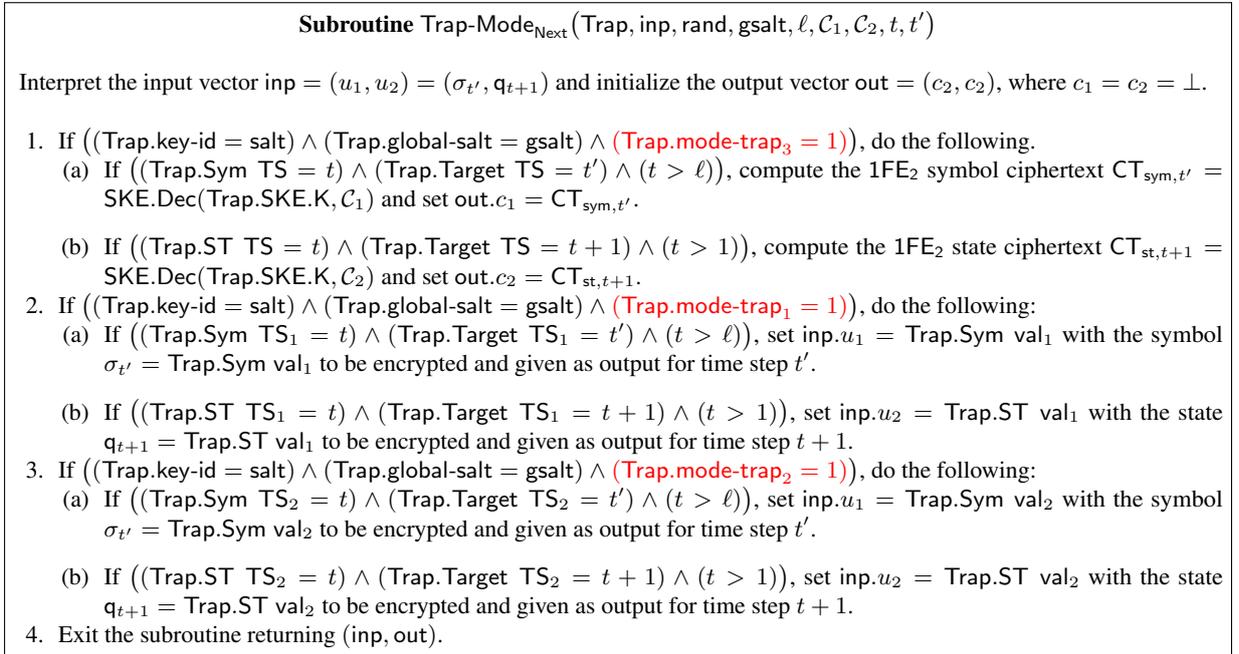


Fig. 10. Subroutine handling the trapdoor modes in Next. This is “active” only in the proof.

1. Invoking kFE.Dec on the ciphertext $\text{CT}_{1,\text{SYM},1}$ encoding the first symbol of \mathbf{w}_1 along with the special ciphertexts $\text{CT}_{\text{ind},\text{SP}}$ encoding $|\mathbf{w}_{\text{ind}}|$ for $\text{ind} \neq 1$ gives $(\text{CT}_{\text{sym},1}, \text{CT}_{\text{st},1})$. By correctness of kFE decryption, we have: $\text{kFE.Dec}(\text{SK}_{\text{Agg}}, (\text{CT}_{1,\text{SYM},1}, \{\text{CT}_{\text{ind},\text{SP}}\}_{\text{ind} \in [k] \setminus \{1\}})) = (\text{CT}_{\text{sym},1}, \text{CT}_{\text{st},1})$.
2. Invoking kFE.Dec on the ciphertext $\text{CT}_{1,\text{SYM},j}$ encoding the j th symbol of \mathbf{w}_1 along with the special ciphertexts $\text{CT}_{\text{ind},\text{SP}}$ encoding $|\mathbf{w}_{\text{ind}}|$ for $\text{ind} \neq 1$ gives $(\text{CT}_{\text{sym},j}, \perp)$. By correctness of kFE decryption, we have: $\text{kFE.Dec}(\text{SK}_{\text{Agg}}, (\text{CT}_{1,\text{SYM},j}, \{\text{CT}_{\text{ind},\text{SP}}\}_{\text{ind} \in [k] \setminus \{1\}})) = (\text{CT}_{\text{sym},j}, \perp)$.
3. Finally, $\forall \text{ind} \in [k] \setminus \{1\}$, invoking kFE.Dec on the ciphertext $\text{CT}_{\text{ind},\text{SYM},j}$ encoding the j th symbol of \mathbf{w}_{ind} along with the special ciphertexts $\text{CT}_{\text{ind}',\text{SP}}$ encoding $|\mathbf{w}_{\text{ind}'}|$ for $\text{ind} \neq \text{ind}'$ computes the new global position of the symbol in the aggregated string and outputs $(\text{CT}_{\text{sym},\tilde{L}_i+j}, \perp)$. By correctness of kFE decryption, we have: $\text{kFE.Dec}(\text{SK}_{\text{Agg}}, (\text{CT}_{\text{ind},\text{SYM},j}, \{\text{CT}_{\text{ind}',\text{SP}}\}_{\text{ind}' \in [k] \setminus \{\text{ind}\}})) = (\text{CT}_{\text{sym},\tilde{L}_i+j}, \perp)$, where $\tilde{L}_i = \sum_{m=1}^{\text{ind}-1} \ell_m$.

Note that $\text{F.Eval}(K_0, (\text{pos} \parallel \text{rand} \parallel \text{gsalt}))$ is the randomness used to compute each of these ciphertext components, where pos refers to the global position specific to a symbol in the aggregate input string.

Correctness of TM Execution. The 1FE scheme supports the functionality $\text{Next} := \text{Next}_{1\text{FE.PK,rand},M,\perp,\perp}$. Let the newly generated and organized sequence of ciphertexts based on time steps be as follows: $((\text{CT}_{\text{sym},1}, \text{CT}_{\text{st},1}), \{\text{CT}_{\text{sym},i}\}_{i \in [2,L_k]})$ with $L_k = \sum_{i=1}^k \ell_i$. Let $\mathbf{w} = (w_1, w_2, \dots, w_{\ell_1}, w_{\ell_1+1}, w_{\ell_1+2}, \dots, w_{\ell_1+\ell_2}, \dots, w_{L_k})$ be the aggregated input string and define $\tau = \text{runtime}(M, \mathbf{w})$. For any time step $t \in [\tau - 2]$, we have

1. Let $t \in [\tau - 2] \setminus [\ell]$. If the current work tape cell was accessed¹², at some time step $\tilde{t} < t$, then $\text{CT}_{\text{sym},t}$ encoding $(\text{SYM}, \text{key-id}, K_{t+1}, t, \ell, \text{gsalt}, \sigma_t, \text{Trap})$ was constructed at time step \tilde{t} . Note that σ_t may be the blank symbol β . When $t \in [\ell]$, $\text{CT}_{\text{sym},t}$ is constructed at time step t via the Agg circuit.
2. The ciphertext component $\text{CT}_{\text{st},t}$ encoding $(\text{ST}, q_t, \text{gsalt})$ at time step t was constructed at time step $t - 1$ for $t > 1$ and at time step 1, when $t = 1$.
3. The randomness $r_t = \text{F.Eval}(K_{\tilde{t}+1}, (t \parallel \text{rand} \parallel \text{gsalt})) = \text{F.Eval}(K_t, (t \parallel \text{rand} \parallel \text{gsalt}))$ binds $\text{CT}_{\text{sym},t}$ and $\text{CT}_{\text{st},t}$ and both the encoded messages also share the same global salt.

Thus, at any given time step $t \in [\tau - 2]$, we have a complete ciphertext of 1FE which may be fed again with SK_{Next} to 1FE.Dec in order to proceed with the computation. Thus, the execution of 1FE.Dec at the $(\tau - 2)$ th time step provides the complete pair $(\text{CT}_{\text{sym},\tau-1}, \text{CT}_{\text{st},\tau-1})$. By the correctness of 1FE scheme again, at time step $t = \tau - 1$, invoking $\text{1FE.Dec}(\text{SK}_{\text{Next}}, (\text{CT}_{\text{sym},\tau-1}, \text{CT}_{\text{st},\tau-1}))$ outputs either “Accept” or “Reject” by simulating the execution of M for the final time step τ inside the function Next , thus correctly outputting $M(\mathbf{w})$.

4.3 Proof of Security for multi-input TMFE

Security of the above construction follows the same blueprint as the proof in Section 3 except that instead of single input functionality ReRand , we now use a k -input functionality Agg to aggregate and rerandomize the inputs. We emphasize that the outputs produced by the Agg functionality are *exactly the same* as the outputs produced by ReRand functionality in Section 3: namely a sequence of 1FE ciphertexts encoding the symbol and global position, computed using randomness derived from a cPRF . Hence, the chief new ingredient in the security proof is the security of Agg functionality, which is derived from the security of the kFE scheme.

Formally, we argue that:

Theorem 2. *Assume that the k input FE for circuits kFE satisfies standard indistinguishability, and the single input FE for circuits 1FE satisfies distributional indistinguishability. Assume that the cPRF*

¹² We assume that every time a cell is accessed, it is written to, by writing the same symbol again if no change is made.

is secure according to definition. Then, the above construction of k input kTMFE satisfies standard indistinguishability.

The proof follows the outline of the single input case, except that now we must additionally keep track of multiple execution threads corresponding to various combinations of ciphertexts across multiple users, i.e. various “global salt” values. In more detail, if each of k users makes Q ciphertext requests, then we have Q^k total possible combinations of ciphertexts, each yielding a different execution thread per key. Note that each of the Q^k combinations is identified with a unique “global salt”. We will assume w.l.o.g that there is a lexicographic ordering on all the global salt values; this can be easily ensured by associating a counter value with each random salt. We do not explicitly include this for notational brevity.

In the single input case, we replaced the execution chain of a machine over an input string from $b = 0$ to $b = 1$, step by step, and enumerated over all keys. Now, we again replace an execution chain step by step as in the single input case, but additionally enumerate over all Q^k combinations for each key, as well as over all keys as before. The number of hybrids grows multiplicatively by Q^k . Details are again deferred to in the full version [1].

5 Indistinguishability Obfuscation for Turing Machines

In this section we construct indistinguishability obfuscation for Turing machines with bounded length input, i.e. the input length $n = n(\lambda)$ is any fixed polynomial in the security parameter. To support inputs of length n , we need an $(n + 1)$ -ary miFE for Turing machines denoted as $(n + 1)$ -TMFE; we instantiate this with our construction from Section 4.

5.1 Construction

Let $\mathcal{M} = \{\mathcal{M}_\lambda\}_{\lambda \in \mathbb{N}}$ denote an ensemble of Turing machines with alphabet $\Sigma_\lambda = \{0, 1\}$. Let $\text{Encode} = \{\text{Encode}_\lambda : \mathcal{M}_\lambda \rightarrow \Sigma_{\text{enc}}^*\}_{\lambda \in \mathbb{N}}$ be an ensemble of encoding schemes for \mathcal{M} on alphabet Σ_{enc} such that for any $M \in \mathcal{M}_\lambda$, $\text{Encode}_\lambda(M) = \langle M \rangle$. Further, let $\mathcal{U} = \{\mathcal{U}_\lambda\}_{\lambda \in \mathbb{N}}$ denote the set of Universal Turing machines parameterized by the security parameter with alphabet $\Sigma_{\mathcal{U}} = \Sigma_{\text{enc}} \cup \Sigma_\lambda$ such that for all $\lambda \in \mathbb{N}$, for any $M \in \mathcal{M}_\lambda$ and any $\mathbf{x} = (x_1, \dots, x_n) \in \Sigma_\lambda^n$, $\mathcal{U}_\lambda(\mathbf{x}, \langle M \rangle)$ takes \mathbf{x} and an encoding $\langle M \rangle$ of M , simulates M on \mathbf{x} and outputs $M(\mathbf{x})$.

Let $(n + 1)$ -TMFE denote the $(n + 1)$ -ary multi-input functional encryption scheme for Turing machines with alphabet $\Sigma_{\mathcal{U}}$. We construct an ensemble of indistinguishability obfuscators $\text{iO} = \{\text{iO}_\lambda\}_{\lambda \in \mathbb{N}}$ with $\text{iO}_\lambda = (\text{iO}.\text{Obf}, \text{iO}.\text{Eval})$ for \mathcal{M}_λ with inputs $\mathbf{x} \in \Sigma_\lambda^n$ as follows.

$\text{iO}.\text{Obf}(1^\lambda, 1^n, M)$: On input the security parameter λ , a bound $n \in \mathbb{N}$ and a Turing machine $M \in \mathcal{M}_\lambda$, do the following:

1. Compute the encoding of M as $\text{Encode}_\lambda(M) = \langle M \rangle$.
2. Compute a master secret key $\text{MSK} \leftarrow (n + 1)\text{-TMFE}.\text{Setup}(1^\lambda, 1^{n+1})$.
3. Compute the secret key for machine \mathcal{U}_λ as $\text{SK}_{\mathcal{U}} \leftarrow (n + 1)\text{-TMFE}.\text{KeyGen}(\text{MSK}, \mathcal{U}_\lambda)$.
4. For $i \in [n]$, compute the encryptions $\text{CT}_i^b = (n + 1)\text{-TMFE}.\text{Enc}(\text{MSK}, (b, i))$, $b \in \Sigma_\lambda$.
5. Compute the encoding of M as $\text{CT}_{n+1} = (n + 1)\text{-TMFE}.\text{Enc}(\text{MSK}, (\langle M \rangle, n + 1))$.
6. Output the obfuscated machine as $\widetilde{M} = (\text{SK}_{\mathcal{U}}, (\{\text{CT}_i^b\}_{i \in \{1, \dots, n\}, b \in \Sigma_\lambda}, \text{CT}_{n+1}))$.

$\text{iO}.\text{Eval}(\widetilde{M}, \mathbf{x})$: On input the obfuscated machine \widetilde{M} and an input $\mathbf{x} \in \Sigma_\lambda^n$, do the following:

1. Parse $\widetilde{M} = (\text{SK}_{\mathcal{U}}, (\{\text{CT}_i^b\}_{i \in \{1, \dots, n\}, b \in \Sigma_\lambda}, \text{CT}_{n+1}))$ and $\mathbf{x} = (x_1, \dots, x_n)$.
2. Compute and output $(n + 1)\text{-TMFE}.\text{Dec}(\text{SK}_{\mathcal{U}}, (\text{CT}_1^{x_1}, \dots, \text{CT}_n^{x_n}, \text{CT}_{n+1}))$.

Correctness is directly followed by the correctness of $(n + 1)$ -TMFE scheme. Since the $(n + 1)$ -TMFE we use is compact, the obfuscation size obtained by the above scheme is $\text{poly}(\lambda, |\mathcal{U}|, |M|, n)$. In the full version [1], we show that our construction is secure:

Theorem 3. Assume that $(n+1)$ -TMFE is a 1-key, 2-ciphertext selectively secure $(n+1)$ -ary multi-input functional encryption scheme for Turing machines which satisfies standard indistinguishability. Then the construction in Section 5.1 is a secure indistinguishability obfuscator for the Turing machines with bounded input length n .

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A Construction: Decomposable FE for Circuits

Given any single-input circuit FE scheme 1FE satisfying standard indistinguishability based security, a *projective* garbled circuit scheme $GC = (GCirc, GInp, GEval)$ with indistinguishability based security [35] supporting a circuit class $\mathcal{C} = \{\mathcal{C}_\lambda\}_{\lambda \in \mathbb{N}}$ with n -bit inputs, a simple PRF $F = (F.Setup, F.Eval)$ and a symmetric encryption scheme SYM, we can construct a single-input decomposable FE scheme DFE supporting the circuit class \mathcal{C} . We note that projective garbled circuit schemes satisfying indistinguishability based security are implied from one-way functions [35].

$DFE.Setup(1^\lambda, 1^n)$: On input the security parameter λ and input message size n , do the following:

1. Generate $(1FE.PK, 1FE.MSK) \leftarrow 1FE.Setup(1^\lambda, 1^{2\lambda+\log n+2})$.
 2. Output $(PK, MSK) = (1FE.PK, 1FE.MSK)$.
- DFE.Enc(PK, \mathbf{x}): On input the public key PK and a message $\mathbf{x} = (x_1, \dots, x_n)$ of length $n = |\mathbf{x}|$, do the following:
1. Sample a PRF key $K \leftarrow F.Setup(1^\lambda)$ and set a flag $mode = 0$.
 2. Compute $CT_{x_i} = 1FE.Enc(PK, (K, \mathbf{0}, i, x_i, mode))$, $\forall i \in [n]$ and output $CT_{\mathbf{x}} = \{CT_{x_i}\}_{i \in [n]}$.
- DFE.KeyGen(MSK, C): On input the master secret key MSK and a circuit $C \in \mathcal{C}_\lambda$, do the following:
1. Sample a random salt $\leftarrow \{0, 1\}^\lambda$, $CT_i \leftarrow \{0, 1\}^{\ell(\lambda)}$, $\forall i \in [0, n]$.
 2. Output $SK_{\hat{C}} = 1FE.KeyGen(MSK, \hat{C}_{C, salt, \{CT_i\}_{i \in [n]}, CT_0})$, where $\hat{C}_{C, salt, \{SYM.CT_i\}_{i \in [n]}, SYM.CT_{\tilde{C}}}$ is a circuit described in Figure 11.

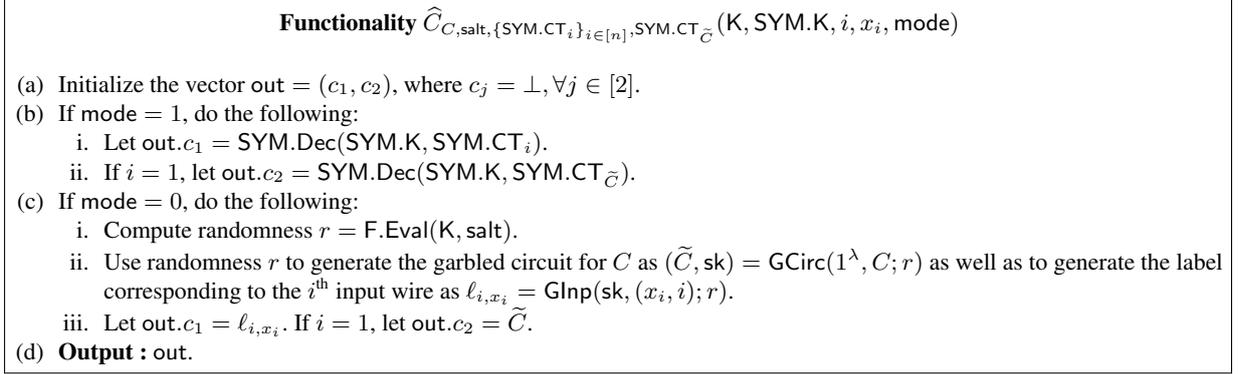


Fig. 11. Functionality $\hat{C}_{C, salt, \{SYM.CT_i\}_{i \in [n]}, SYM.CT_{\tilde{C}}}$

- DFE.Dec($SK_{\hat{C}}$, $CT_{\mathbf{x}}$): On input a function key $SK_{\hat{C}}$ and a decomposed ciphertext $CT_{\mathbf{x}} = \{CT_{x_i}\}_{i \in [n]}$, do the following:
1. For $i = 1$, invoke $1FE.Dec(SK_{\hat{C}}, CT_{x_1})$ to obtain a pair $(\ell_{1, x_1}, \tilde{C})$.
 2. For all $i \in [2, n]$, invoke $1FE.Dec(SK_{\hat{C}}, CT_{x_i})$ to obtain (ℓ_{i, x_i}, \perp) .
 3. Note that $\tilde{\mathbf{x}} = \{\ell_{i, x_i}\}_{i \in [n]}$ represents the labels corresponding to the garbled input underlying $CT_{\mathbf{x}}$ generated as outputs of \hat{C} , while \tilde{C} represents the garbled circuit for C .
 4. Run $GEval(\tilde{C}, \tilde{\mathbf{x}})$ to get \mathbf{y} .

Correctness. We have by correctness of $1FE.Dec$ that it outputs the garbled input $\tilde{\mathbf{x}}$ and the garbled circuit \tilde{C} correctly. The correctness of $GEval$ implies that decryption recovers $C(\mathbf{x})$ as desired.

The proof of security is provided in the full version [1].