On the Practicality of Post-Quantum TLS Using Large-Parameter CSIDH

Fabio Campos^{1,2}, Jorge Chavez-Saab^{3,4}, Jesús-Javier Chi-Domínguez⁴, Michael Meyer⁵, Krijn Reijnders², Francisco Rodríguez-Henríquez^{3,4}, Peter Schwabe^{2,6}, and Thom Wiggers⁷

- ¹ RheinMain University of Applied Sciences, Wiesbaden, Germany campos@sopmac.de
 - ² Radboud University, Nijmegen, The Netherlands krijn@cs.ru.nl
- ³ Departamento de Computación, CINVESTAV-IPN, Mexico
 ⁴ Cryptography Research Center, Technology Innovation Institute, Abu Dhabi, United Arab Emirates

 ${\tt jorge.saab@tii.ae,\ jesus.dominguez@tii.ae,\ francisco.rodriguez@tii.ae}$ ${\tt University\ of\ Regensburg,\ Germany}$

michael@random-oracles.org

Max Planck Institute for Security and Privacy, Bochum, Germany peter@cryptojedi.org
PQShield, Nijmegen, The Netherlands thom@thomwiggers.nl

Abstract. The isogeny-based scheme CSIDH is considered to be the only efficient post-quantum non-interactive key exchange (NIKE) and poses small bandwidth requirements, thus appearing to be an attractive alternative for classical Diffie-Hellman schemes. A crucial CSIDH design point, still under debate, is its quantum security when using prime fields of 512 to 1024 bits. Most work has focused on prime fields of that size and the practicality of CSIDH with large parameters, 2000 to 9000 bits, has so far not been thoroughly assessed, even though analysis of quantum security suggests these parameter sizes. We fill this gap by providing two CSIDH instantiations: A deterministic and dummyfree instantiation based on SQALE, aiming at high security against physical attacks, and a speed-optimized constant-time instantiation that adapts CTIDH to larger parameter sizes. We provide implementations of both variants, including efficient field arithmetic for fields of such size, and high-level optimizations. Our deterministic and dummy-free version, dCSIDH, is almost twice as fast as SQALE, and, dropping determinism, CTIDH at these parameters is thrice as fast as dCSIDH. We investigate their use in real-world scenarios through benchmarks of TLS

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using our software. Although our instantiations of CSIDH have smaller communication requirements than post-quantum KEM and signature schemes, both implementations still result in too-large handshake latency (tens of seconds), which hinder further consideration of using CSIDH in practice for conservative parameter set instantiations.

Keywords: post-quantum cryptography, isogenies, CSIDH, TLS

1 Introduction

The commutative isogeny-based key exchange protocol (CSIDH) was proposed by Castryck, Lange, Martindale, Panny, and Renes [22] at Asiacrypt 2018. Although it was proposed too late to be included as a candidate in the NIST post-quantum standardization effort [51], it has since received significant attention from the post-quantum-crypto research community. From a crypto-engineering point of view, this attention can be explained by two unique features of CSIDH: Firstly, with the originally proposed parameters, CSIDH has remarkably small bandwidth requirements. Specifically, CSIDH-512, the parameter set targeting security equivalent to AES-128, needs to transmit only 64 bytes each way, more than a factor of 3 less than SIKEp434_compressed, the most bandwidth-efficient competitor submitted to the NIST project. Secondly—and more importantly—CSIDH is so far the only realistic option for post-quantum non-interactive key exchange. This means that it could in principle be used as a post-quantum drop-in replacement for Diffie-Hellman key exchange.

Unfortunately, quite soon after CSIDH was proposed, several security analyses called into question the claimed concrete security against quantum attacks achieved by the proposed parameters [12, 24, 53]. The gist of these analyses seems troublesome; for example, [53] states that "the cost of CSIDH-512 key recovery is only about 2¹⁶ quantum evaluations using 2⁴⁰ bits of quantumly accessible classical memory (plus relatively small other resources)". On the other hand, [12] claims a cost of 2¹⁹ quantum evaluations for attacking the same instance, but proposes a quantum circuit requiring only 2^{52.6} T-gates per evaluation, which means the security is still insufficient. Upon exploring the quantum cost of attacking larger instances but ignoring the cost per CSIDH quantum evaluation, [24] found that instances ranging from 2048 to 4096 bits may be necessary to achieve the security level originally claimed by CSIDH-512.

Interestingly, although some of these concerns were raised as early as May 2018 (i.e., at a time when [22] was available only as preprint), most research on efficient implementations [5, 23, 48, 52], side-channel attacks [17], and fault attacks against CSIDH [7, 16, 44] continued to work with the original parameters. This can probably partly be explained by the fact that the software implementation referenced in [22]⁸ implements only the smaller two of the three original parameter sets, i.e., CSIDH-512 and CSIDH-1024. However, another reason is that the concerns about the quantum security of CSIDH were (and to some extent still

⁸ Available from https://yx7.cc/code/csidh/csidh-latest.tar.xz

are) subject of debate. Most notably, Bernstein, Lange, Martindale, and Panny [9] point out that one issue with quantum attacks against CSIDH is the rather steep cost of implementing CSIDH on a quantum computer in the first place. Their analysis of quantum attacks against CSIDH-512 concludes that "the cost of each query pushes the total attack cost above 2^{80} ".

In this paper we do not take any position in this ongoing debate, but rather set out to answer the question of what it means for CSIDH performance and applicability if we choose more conservative parameters to resist quantum attacks in the cost models used by [12, 24, 53].

Contributions of this paper. On a high level, the contributions of this paper are two-fold: First, we present optimized software for CSIDH at high(er) security levels; second, we benchmark CSIDH as a Diffie-Hellman replacement in cryptographic protocols. The software we present here starts from the analysis and parameter sizes proposed in [24] to reach NIST security levels 1 (equivalent AES-128) and 3 (equivalent AES-192) under different assumptions about the efficiency of quantum attacks. However, our results go much further with regard to optimizing parameters and implementation techniques than [24]:

- 1. Efficient CSIDH instantiations: We present parameter choices following two different approaches of implementing CSIDH with large parameters:
 - The first approach aims at easy (verification of) side-channel protections, and is based on SQALE [24]. In this approach we eliminate randomness requirements and the use of dummy operations in the CSIDH computation by restricting private keys to be drawn from $\{-1,1\}^n$. This was proposed by Cervantes-Vázquez, Chenu, Chi-Domínguez, De Feo, Rodríguez-Henríquez, and Smith [23], who already mentioned that this approach becomes more natural for larger CSIDH parameters. We refer to this deterministic version of CSIDH as dCSIDH.
 - The second approach optimizes purely for performance and uses the CTIDH batching techniques introduced in [5]. We refer to this variant of CSIDH as CTIDH. In particular, we extend the implementation from [5] to larger parameter sets.
- 2. Optimizations of dCSIDH and CTIDH: We present a faster key validation approach for large parameters, and discuss how adding a small number of bits to public keys improves the shared-key generation in dCSIDH.
- 3. Fast finite field arithmetic: All our implementations use curves over prime fields \mathbb{F}_p , with $p = f \cdot \prod_{i=1}^n \ell_i 1$, where ℓ_i are distinct odd primes and f is a power of 2. The sizes of p range from 2048 to 9216 bits. We carefully optimize arithmetic in these fields for 64-bit Intel processors, specifically the Skylake microarchitecture. We present implementations of three different options for the underlying field arithmetic, namely,
 - (a) an approach based on the GNU Multiple Precision Library (GMP)
 - (b) MULX-based multiplier using the schoolbook approach (OpScan)
 - (c) MULX-based multiplier using the Karatsuba approach (Karatsuba)

- 4. Performance evaluation: We combine the contributions in an optimized C/assembly library outperforming the software presented in [24] by a factor up to 2.53× for dCSIDH. Our CSIDH library follows the "constant-time" paradigm for cryptographic implementations and thus protects against timing attacks by avoiding secret-dependent branch conditions and memory indices.
- 5. Performance in practice: In order to evaluate the performance of our CSIDH software in the context of network protocols, we extend the Rustls library [11] to support OPTLS [38], which uses a NIKE for early authentication. We integrate our CSIDH software as a replacement for Diffie-Hellman and compare the performance of the resulting post-quantum OPTLS to post-quantum KEMTLS as proposed in [58].

Related work. The impact that the proposal of CSIDH has produced in the community can be assessed by the many papers that have been produced around this protocol. Since Castryck, Lange, Martindale, Panny, and Renes [22] left open the problem of implementing CSIDH in constant-time, several papers were published proposing different strategies for achieving this property.

The first constant-time implementation of CSIDH was reported by Bernstein, Lange, Martindale, and Panny [9]. Their analysis focused on assessing the quantum security level provided by CSIDH. For this purpose, they strove for producing not only a constant-time CSIDH instantiation but also a randomness-free implementation of it. Meyer, Campos, and Reith [48] (see also [49]) presented a more efficient constant-time instantiation of CSIDH for practical purposes. They introduced several algorithmic tricks, including the SIMBA technique, and sampling secret keys from varying intervals. This was further improved by Onuki, Aikawa, Yamazaki, and Takagi [52], who proposed to keep track of two points to evaluate the action of an ideal: one in $E(\mathbb{F}_p)$, and one in $E(\mathbb{F}_{p^2})$ with its x-coordinate in \mathbb{F}_p . Moreover, Moriya, Onuki, and Takagi [50], and Cervantes-Vázquez et al. [23], performed more efficient CSIDH isogeny computations using the twisted Edwards model of elliptic curves. The authors of [23] proposed a more computationally demanding dummy-free variant of CSIDH, which in exchange, is arguably better suited to resist physical attacks from stronger adversaries, such as fault attacks.

In a second wave of studies around CSIDH, several crucial building blocks were improved. [26, 34] presented a framework that permits to adapt the optimal strategies of SIDH/SIKE into the context of CSIDH. The computation of large degree isogenies using an improved version of Vélu's formulas known as $\sqrt{\text{élu}}$ [8], was exploited in [1, 5]. Variants of CSIDH were reported in [19, 20, 25].

A breakthrough in the performance of constant-time CSIDH was achieved by Banegas, Bernstein, Campos, Chou, Lange, Meyer, Smith, and Sotáková [5], resulting in an almost twofold speedup. They introduce a variant, named CTIDH, using a new key space and accompanying constant-time algorithms that exploit the idea of batching isogeny degrees. However, the performance evaluation of [5] is restricted to primes of 512 and 1024 bits. The authors of [24] presented SQALE, the first CSIDH implementation at higher security levels going all the way from primes of size 2000 bits up to 9000 bits.

CSIDH is not the only attempt at building a post-quantum NIKE. SIDH [28, 35] was first introduced as a NIKE, but was shown to be insecure in the static-static scenario [31]. At the cost of many parallel executions of SIDH, this was addressed in [3], before SIDH was completely broken in [18, 46, 56]. The only post-quantum NIKE that is not based on isogenies is based on (R/M)LWE and, according to Lyubashevsky, goes back to "folkore" [45]. Such a NIKE was first analyzed in [37]. A more concrete instantiation of this approach is the recently proposed SWOOSH [30]. We discuss differences between CSIDH and SWOOSH in a bit more detail in Section 7.

Availability of software. We place our CSIDH software into the public domain (CC0). All software described in this paper and all measurement data from the TLS experiments are available at https://github.com/kemtls-secsidh/code.

Organization of this paper. Section 2 presents the necessary background on isogeny-based cryptography and introduces CSIDH and its CTIDH instantiation. Section 3 explains how we instantiate dCSIDH and CTIDH and choose parameters for our optimized implementations. Section 4 introduces algorithmic optimizations that apply to our instantiations of dCSIDH and CTIDH. Section 5 details our optimization techniques for finite field arithmetic, in particular the efficient Karatsuba-based field arithmetic, and presents benchmarking results for the group action evaluation for dCSIDH and CTIDH. Section 6 describes our integration of dCSIDH and CTIDH into OPTLS and presents handshake performance results. Finally, Section 7 concludes the paper and sketches directions for future work.

2 Preliminaries

2.1 NIKEs vs. KEMs

We briefly recall the definitions of non-interactive key exchange (NIKE) and key-encapsulation mechanism (KEM) as follows:

Definition 1. A non-interactive key exchange (NIKE) is a collection of two algorithms, KeyGen and SharedKey, where

- KeyGen is a probabilistic algorithm that on input 1^k , where k is a security parameter, outputs a keypair (sk, pk); and
- SharedKey is a deterministic algorithm that on input a public key pk and a secret key sk outputs a shared key K.

A NIKE is correct if for any $(\mathsf{sk}_1, \mathsf{pk}_1) \leftarrow \mathsf{KeyGen}(1^k)$ and $(\mathsf{sk}_2, \mathsf{pk}_2) \leftarrow \mathsf{KeyGen}(1^k)$ it holds that $\mathsf{SharedKey}(\mathsf{pk}_1, \mathsf{sk}_2) = \mathsf{SharedKey}(\mathsf{pk}_2, \mathsf{sk}_1)$.

Definition 2. A key-encapsulation mechanism (KEM) is a collection of three algorithms, KeyGen, Encaps, and Decaps, where

- KeyGen is a probabilistic algorithm that on input 1^k , where k is a security parameter, outputs a keypair (sk, pk); and

- Encaps is a probabilistic algorithm that on input a public key pk outputs a ciphertext ct and a shared key K.
- Decaps is a deterministic algorithm that on input a ciphertext ct and a secret key sk outputs a shared key K.

A KEM is correct if for any $(\mathsf{sk}, \mathsf{pk}) \leftarrow \mathsf{KeyGen}(1^k)$ and $(\mathsf{ct}, K) \leftarrow \mathsf{Encaps}(\mathsf{pk})$ it holds that $\mathsf{Decaps}(\mathsf{ct}, \mathsf{sk}) = K$.

Both NIKEs and KEMs can be used for key exchange, but the non-interactive nature of a NIKE makes it more flexible than a KEM. In the context of their use in protocols, there are three different scenarios:

- 1. Some scenarios naturally use a KEM. Those scenarios can alternatively also use a NIKE, but they do not benefit in any way from the non-interactive nature of a NIKE. An example for this scenario is the ephemeral key exchange in TLS 1.3, which currently uses (EC)DH, but will easily migrate to post-quantum KEMs [13, 14, 41, 42, 61].
- 2. Some protocols, most notably the X3DH protocol in Signal [47] have to use a NIKE and cannot replace this NIKE by a KEM. The reason is that this protocol cannot assume communication partners to be online at the same time and critically relies on the non-interactive nature of a NIKE.
- 3. Some protocols are somewhat in between: they can be designed from KEMs only, but this comes at the cost of more communication rounds. This has been discussed in some detail in the design of post-quantum Noise [2] and also in the context of the NIKE-based OPTLS [38] vs. the KEM-based KEMTLS [58]. We will revisit the comparison of these two protocols in a post-quantum context in more detail in Section 6.

2.2 The CSIDH NIKE

Background. Let \mathbb{F}_p be a finite field of prime order p, such that $p+1=f\cdot g\cdot \prod_{i=1}^n\ell_i$, where each ℓ_i is a small odd prime, $f\geq 4$ is a power of two, and g is a cofactor guaranteeing that p is prime. Now consider the set of supersingular elliptic curves over \mathbb{F}_p , i.e., the elliptic curves with p+1 \mathbb{F}_p -rational points. We will represent these curves in the Montgomery model, i.e., through an equation of the form

$$E_A : y^2 = x^3 + Ax^2 + x, \quad A \in \mathbb{F}_p.$$
 (1)

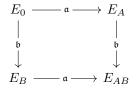
This is possible since the group order (p+1) is a multiple of 4. In the context of CSIDH we are interested in *isogeny graphs of degree* N, denoted $\mathcal{G}_N(\mathbb{F}_p)$. The vertices of such graphs are precisely the supersingular curves over \mathbb{F}_p ; the edges are \mathbb{F}_p -rational isogenies of degree N. CSIDH relies on the following property: for each small odd prime ℓ_i dividing p+1, a supersingular curve E_A has only two (supersingular) neighbors in the isogeny graph $\mathcal{G}_{\ell_i}(\mathbb{F}_p)$ (i.e., isogenies over \mathbb{F}_p of degree ℓ_i). We can uniquely describe these isogenies by their kernels: The unique cyclic subgroup of order ℓ_i of $E_A(\mathbb{F}_p)$ defines the isogeny from E_A to one of these

neighbors $E_{A'}$. This cyclic subgroup can be described by any of its generators, which in this case means that finding a point in $E_A(\mathbb{F}_p)$ of order ℓ_i is enough to describe an isogeny of degree ℓ_i . As $E_{A'}$ is again supersingular, $E_{A'}(\mathbb{F}_p)$ has order p+1 as well and hence a unique cyclic subgroup of order ℓ_i , which gives an isogeny to the unique neighbor that is not E_A . The general action of moving in this direction in this graph $\mathcal{G}_{\ell_i}(\mathbb{F}_p)$ using the unique subgroup of order ℓ_i is denoted by \mathfrak{l}_i , and the curve $E_{A'}$ that is reached from E_A by this action is denoted $\mathfrak{l}_i * E_A$. In short, \mathfrak{l}_i represents one step in the isogeny graph $\mathcal{G}_{\ell_i}(\mathbb{F}_p)$, and each small odd prime ℓ_i dividing p+1 gives us such an \mathfrak{l}_i . Steps in $\mathcal{G}_{\ell_i}(\mathbb{F}_p)$, represented by \mathfrak{l}_i , are commutative, so that applying \mathfrak{l}_i to $\mathfrak{l}_j * E_A$ is the same as applying \mathfrak{l}_j to $\mathfrak{l}_i * E_A$ for different degrees ℓ_i and ℓ_j . We can also compute steps in the other direction, which is denoted by $\mathfrak{l}_i^{-1} * E_A$. The subgroup of points of order ℓ_i with x-coordinate in \mathbb{F}_p and y-coordinate in $\mathbb{F}_{p^2} \backslash \mathbb{F}_p$ uniquely defines the corresponding isogeny kernels. Applying both \mathfrak{l}_i and \mathfrak{l}_i^{-1} effectively cancels out, i.e., we have $\mathfrak{l}_i * (\mathfrak{l}_i^{-1} * E) = \mathfrak{l}_i^{-1} * (\mathfrak{l}_i * E) = E$.

The CSIDH scheme. The CSIDH scheme [22] unrolls naturally from the action described above: The secret key is a vector of n integers (e_1, \ldots, e_n) defining the product $\mathfrak{a} = \prod_{i=1}^n \mathfrak{l}_i^{e_i}$. In the original proposal the integers e_i are chosen from $\{-m,\ldots,m\}$ for some $m \in \mathbb{N}$, which results in a key space of size $(2m+1)^n$. The public key is the supersingular curve E_A which corresponds to the secret key \mathfrak{a} applied to a publicly known starting curve E_0 :

$$E_A = \mathfrak{a} * E_0 = \mathfrak{l}_1^{e_1} * \dots * \mathfrak{l}_n^{e_n} * E_0. \tag{2}$$

This public key E_A can be encoded by the single value $A \in \mathbb{F}_p$ (see Equation (1)). Shared-key computation is the same as public-key computation, except that instead of the public parameter E_0 it uses a public key E_A as input curve. That is, Alice and Bob compute their shared secret by calculating $E_{AB} = \mathfrak{a} * E_B = (\mathfrak{a} \cdot \mathfrak{b}) * E_0$ and $E_{BA} = \mathfrak{b} * E_A = (\mathfrak{b} \cdot \mathfrak{a}) * E_0$, respectively, with $E_{AB} = E_{BA}$ thanks to the commutativity. This is summarized by the following diagram:



Computing the group action $\mathfrak{a} * E$. Straightforward high-level pseudocode for the computation of the group action $\mathfrak{a} * E$ is given in Algorithm 1. The dominating cost is the construction and evaluation of the ℓ_i -isogenies corresponding to the action of the \mathfrak{l}_i (Lines 5 and 7), which in turn decompose into a sequence of operations in \mathbb{F}_p . However, the high-level view also illustrates an additional complication for secure implementations of CSIDH, namely that the number of iterations of the inner loop (Line 3) and the direction of the isogenies corresponding to the action of \mathfrak{l}_i (Line 4) depend on the secrets e_i and naive implementations thus leak secret information through timing.

Algorithm 1 High-level view of the CSIDH group action computation.

```
Input: I \in \mathbb{F}_p defining a curve E_I
Input: secret key (e_1, \ldots, e_n)
Output: R \in \mathbb{F}_p defining a curve E_R = \mathfrak{l}_1^{e_1} * \cdots * \mathfrak{l}_n^{e_n} * E_I
 1: E_R \leftarrow E_I
 2: for i from 1 to n do
 3:
          for j from 1 to |e_i| do
 4:
               if e_i > 0 then
                    E_R \leftarrow \mathfrak{l}_i * E_R
 5:
 6:
                    E_R \leftarrow \mathfrak{l}_i^{-1} * E_R
 7:
 8:
 9:
          end for
10: end for
11: return R
```

For constant-time behavior, we need to be careful not to leak this information on e_i . Current implementations of CSIDH hide e_i by computing m isogenies per degree ℓ_i , while effectively performing $|e_i|$ isogenies, e.g., by using dummy computations or computations that effectively cancel each other such as $l_i * l_i^{-1} * E$.

For the sake of simplicity, Algorithm 1 omits the description of several underlying building blocks. For example, the computation of an isogeny of degree ℓ_i requires as input a point of order ℓ_i . Points of a prescribed order can be obtained probabilistically by sampling random points on the current curve. Any randomly sampled point T can generate exactly one isogeny of those degrees ℓ_i that divide the order of T, by pushing T through such isogenies to get a similar point T on the codomain curve. The order in which we perform such ℓ_i -isogenies giving a point T that can perform multiple of them influences the performance. Hence, different *strategies*, i.e. orderings of ℓ_i -isogenies, point evaluations, and point multiplications, can affect performance. Several efficient strategies are described in, e.g., [22, 26]. We describe our choices for the CSIDH group action computation in more detail in Section 3 and Section 4.

Computing a single isogeny $E_R \leftarrow \mathfrak{l}_i * E_R$. A single isogeny $E_R \leftarrow \mathfrak{l}_i * E_R$ can be computed in multiple ways: Traditionally, the formulas introduced by Vélu [60] are used, at a cost of approximately 6ℓ field multiplications for an isogeny of degree ℓ . In 2020, [8] presented new formulas for constructing and evaluating isogenies of degree ℓ , at a combined cost of just $\tilde{O}(\sqrt{\ell})$ field multiplications, denoted as $\sqrt{\epsilon}$ lu. With respect to CSIDH, [1] reports that the $\sqrt{\epsilon}$ lu formulas of [8] improve the traditional formulas for isogenies of degree $\ell \geq 89$, and concludes that constant-time CSIDH implementations using 511- and 1023-bit primes are moderately improved by the $\sqrt{\epsilon}$ lu formulas. [21] introduced a third method to compute isogenies, specifically those of degree less than 13, by using radical computations. We will not make use of this method, as analysis by [25] shows that this is unfavorable when the base field \mathbb{F}_p is larger than 1024 bits.

2.3 CTIDH

Banegas, Bernstein, Campos, Chou, Lange, Meyer, Smith, and Sotáková [5] proposed a new approach for constant-time CSIDH, named CTIDH. The main novelties are a different way of specifying the key spaces, and some algorithmic adaptions in order to obtain a constant-time algorithm.

CTIDH key spaces. For defining CTIDH keyspaces, we organize the primes ℓ_i in N batches, such that each batch consists of consecutive primes. In particular, we choose a vector of batch sizes $N=(N_1,\ldots,N_B)$ with all $N_i>0$, such that $\sum_{i=1}^B N_i=n$. Then we distribute the n prime degrees ℓ_1,\ldots,ℓ_n among those B batches. That is, we define the first batch as $(\ell_{1,1},\ldots,\ell_{1,N_1}):=(\ell_1,\ldots,\ell_{N_1})$, the second batch as $(\ell_{2,1},\ldots,\ell_{2,N_2}):=(\ell_{N_1+1},\ldots,\ell_{N_1+N_2})$, etc. Accordingly, we relabel the private key elements e_k as $e_{i,j}$.

Instead of directly sampling the key elements $e_{i,j}$ from some interval [-m,m] as in CSIDH, CTIDH only limits the 1-norm of each key batch. That is, for the i-th batch $(\ell_{i,1},\ldots,\ell_{i,N_i})$, we fix a bound m_i and sample corresponding key elements $e_{i,j}$ such that $\sum_{j=1}^{N_i} |e_{i,j}| \leq m_i$. This means that for each isogeny we compute for the i-th batch, its degree could be any of $\ell_{i,1},\ldots,\ell_{i,N_i}$. This adds a combinatorial advantage, in the sense that the same number of isogenies as in CSIDH leads to a much larger key space size in CTIDH. In other words, CTIDH requires a smaller number of isogenies for reaching the same key space size. For example, the fastest previous constant-time implementation of CSIDH-512 with key space size 2^{256} required the computation of 438 isogenies, while the CTIDH parameters of [5] only requires 208 isogenies for the same key space size. For details, we refer to [5]. We note that as defined above, CSIDH is a special case of CTIDH using n batches of size 1.

CTIDH algorithm. The main problem for constant-time implementations with this adapted key space lies in the fact that we must hide the degree of each isogeny from side channels. Given that the computational effort for an isogeny directly depends on its degree, a straightforward implementation of CTIDH would leak the degree of each isogeny. On the other hand, an attacker must not be able to observe to which degree out of $\{\ell_{i,1},\ldots,\ell_{i,N_i}\}$ each isogeny for the *i*-th batch corresponds. [5] achieves this by using an observation from [9]. The usual isogeny formulas [8, 60], have a Matryoshka-doll structure. That is, if $\ell_i < \ell_j$, then an ℓ_j -isogeny performs exactly the computations that an ℓ_i -isogeny would require, plus some extra operations. Therefore, we can easily compute an ℓ_i -isogeny at the cost of an ℓ_j -isogeny, by performing dummy operations for the extra steps. In CTIDH, we use this idea to compute each isogeny for the *i*-th batch $(\ell_{i,1},\ldots,\ell_{i,N_i})$ at the cost of the most expensive degree, i.e., an ℓ_{i,N_i} -isogeny. In this way, the isogeny degrees do not leak via timing channels.

There are several other operations that require adjustments in CTIDH in order to obtain a constant-time implementation. For instance, this includes scalar multiplications that produce points of suitable order, or point rejections, which must occur independently of the required isogeny degree. For details on how these issues are resolved, we refer to [5].

Even though these algorithmic adjustments induce some computational overhead, CTIDH is almost twice as fast as its CSIDH counterpart for the CSIDH-512 and CSIDH-1024 parameter sets from [22] (see [5]).

2.4 Quantum security

While classical security imposes a restriction on the minimum key space size, quantum security usually poses more restrictive requirements. However, it is argued in [24] that for reasonable key spaces (that is, spaces large enough to achieve classical security), the quantum security of CSIDH relies only on the size of the prime p, regardless of the size of the actual key space being used. This is due to the fact that the most efficient quantum attack, Kuperberg's algorithm [40], requires working over a set with a group structure. Since the entire group representing all possible isogenies is of size roughly \sqrt{p} , this attack needs to search a space much larger than the keyspace itself, which only depends on n and the exponent bound m. For example, in the case of CSIDH-512, the element \mathfrak{l}_3 alone generates the entire group of size roughly 2^{257} [10]. It is expected that a handful of \mathfrak{l}_i generate the entire group also for larger instances. In a nutshell, classical security is determined by the size of the key space, whereas quantum security is determined by the size of p, as long as the key space is not chosen particularly badly, e.g., as a small subgroup of the full class group.

3 Proposed instantiations of CSIDH

In this section, we describe how to instantiate and choose parameters for large-parameter CSIDH. We describe two different approaches to selecting parameters: dCSIDH targets a deterministic and dummy-operation-free implementation ¹⁰, whereas CTIDH optimizes for the batching strategies proposed in [5]. This reflects the two extreme choices one can make to either prioritize security against physical attacks or speed. We note that there are several choices in the middle ground, trading off physical security for speed. For comparability, both approaches share the choice of underlying finite fields \mathbb{F}_p , which we detail in Section 3.1.

3.1 The choice of p

In this work, we take the conservative parameter suggestions from [24] at face value. In particular, we consider primes of 2048 and 4096 bits to target NIST security level 1, 5120 and 6144 bits to target NIST security level 2, and 8192 and 9216 bits to target NIST security level 3. Each pair of bitsizes represents a choice between more "aggressive" assumptions (with attacker circuit depth bounded by 2^{60}) or more "conservative" assumptions (attacker circuit depth bounded by

The \mathfrak{l}_i represent elements of the class group $\mathcal{C}\ell(\mathbb{Z}[\sqrt{p}])$, which has size roughly \sqrt{p} .

Our implementation does not take the recent physical attacks [7, 17] into account, whose impact in the high-parameter range is unclear. Heuristically, countermeasures against both attacks should not impact performance by much.

2⁸⁰). As stressed in [24], this choice of parameters does not take into account the cost of calls to the CSIDH evaluation oracle on a quantum computer and is likely to underestimate security. However, as discussed in Section 1, we merely aim at giving performance results for conservative parameters.

All our implementations use primes of the form $p = f \cdot \prod_{i=1}^n \ell_i - 1$, where ℓ_i are distinct odd primes, f is a large power of 2 and n denotes the number of such ℓ_i dividing p+1. For these sizes of p, it becomes natural to pick secret key exponents $e_i \in \{-1, +1\}$, as n can be chosen large enough to reach the desired keyspace size [23, 24]. In particular, to achieve a keyspace of b bits in CSIDH we need to have at least n = b of these ℓ_i in this case.

For conservative instances, we base the keyspace sizes on the classical meetin-the-middle (MITM) attack considered in [22], requiring $b=2\lambda$ for security parameter λ . That is, $b=256,\ 256,\ 384$ for p4096, p6144, p9216, respectively. On the other hand, for aggressive instances we based the keyspace size on the limited-memory van Oorschot-Wiener golden collision search [59] with the assumptions from [24], which leads to $b=221,\ 234,\ 332$ for p2048, p5120, p8192, respectively.

Finally, we restrict to cofactors f for which the power of 2 is a multiple of 64, since the arithmetic optimizations discussed in Section 5 require this shape. Hence, to find optimal primes for our implementation, we let ℓ_1, \ldots, ℓ_b be the b smallest odd primes and then compute the cofactor f as the largest power of 2^{64} that fits in the leftover bitlength. This still leaves us with a bitlength slightly smaller than the target, and hence the leftover bits can be used to search for additional factors ℓ_i (making n > b) that make $f \cdot \prod_{i=1}^n \ell_i - 1$ a prime number. These extra factors go unused for dCSIDH, where they are viewed as part of the cofactor, but are exploited by the batching strategies of CTIDH to increase performance. We set a minimum requirement of 5 additional ℓ_i factors (that is, $n \geq b + 5$), decreasing f by a single factor of 2^{64} when not enough bits were left over. The results of this search are shown in Table 1.

Table 1: Parameters for reconstructing each prime $p = f \cdot \prod_{i=1}^{n} \ell_i - 1$. In each case the ℓ_i are assumed to be the first n odd primes, excluding some primes and including larger primes ℓ_i to ensure that p is prime. These are given in the Excluded and Included columns.

Prime bits	f	n	Excluded	Included	Key Space	NIST level
p2048	2^{64}	226	{1361}	_	2^{221}	1 (aggressive)
p4096	2^{1728}	262	${347}$	$\{1699\}$	2^{256}	1 (conservative)
p5120	2^{2944}	244	$\{227\}$	{1601}	2^{234}	2 (aggressive)
p6144	2^{3776}	262	$\{283\}$	$\{1693, 1697, 1741\}$	2^{256}	2 (conservative)
p8192	2^{4992}	338	$\{401\}$	$\{2287, 2377\}$	2^{332}	3 (aggressive)
p9216	2^{5440}	389	$\{179\}$	$\{2689, 2719\}$	2^{384}	3 (conservative)

3.2 Parameters for dummy-free, deterministic dCSIDH

The restriction of exponents to $\{-1, +1\}$ makes it easier to make dCSIDH deterministic and dummy free [23, 24], as we always perform only one isogeny of each degree, with the only variable being the "direction" of each isogeny. Since isogenies in either direction require exactly the same operations, it is easy to obtain a constant-time implementation without using dummy operations.

Randomness appears in the traditional CSIDH implementation: it arises from the fact that performing isogenies of degree ℓ_i requires a point of order ℓ_i as input, and such a point is obtained by sampling random points on the current curve. Any random point can either be used for "positive" steps \mathfrak{l}_i^{-1} or "negative" steps \mathfrak{l}_i^{-1} . Hence, a point of order ℓ_i can be used only once and only for a specific orientation. Doing more than one isogeny of each degree requires us, therefore, to sample new points midway. However, by restricting e_i to $\{-1,+1\}$, we have to compute only one isogeny per degree ℓ_i . This allows us to avoid random sampling by providing a pair of points T_+, T_- beforehand whose orders are divisble by all ℓ_i , where T_+ can be used for the positive steps \mathfrak{l}_i with $e_i=1$, and T_- for the negative steps \mathfrak{l}_i^{-1} , with $e_i=-1$. We refer to such points as full-torsion points, as they allow us to perform an isogeny of every degree ℓ_i by multiplying them by the right scalar. That is, to perform an ℓ_i -isogeny in the "plus" direction, we can use the point $[\frac{p+1}{\ell_i}]$ T_+ of order ℓ_i .

Note that the probability for the order of a random point to contain the factor ℓ_i is given by $\frac{\ell_i-1}{\ell_i}$. Thus, sampling for a pair of full-torsion points can be expensive when small factors ℓ_i are used, as they dominate the probability $\prod \frac{\ell_i-1}{\ell_i}$ of sampling a full-torsion point. Since the primes we use always have additional ℓ_i factors that are unused in dCSIDH (see Section 3.1), we make point sampling more efficient by always discarding the smallest primes rather than the largest ones, increasing the odds to sample a full-torsion point. For example, the prime p4096 has 262 ℓ_i factors but only needs a keyspace of 2^{256} , hence we can discard 6 primes. By discarding the 6 smallest ones, the probability to sample a full-torsion point goes up from $\prod_{i=1}^{256} \frac{\ell_i-1}{\ell_i} \approx 0.151$ to $\prod_{i=7}^{262} \frac{\ell_i-1}{\ell_i} \approx 0.418$, making it more than 2.7 times as easy to sample full-torsion points T_+ and T_- . Such a shift in primes causes a trade-off in the rest of the protocol, as higher-degree isogenies are more expensive. However, due to the improvements in [8], the extra cost of using $\ell_{257}, \ldots, \ell_{262}$ instead of ℓ_1, \ldots, ℓ_6 is relatively small in comparison to the total cost of a group action computation. Thus, discarding the smallest ℓ_i is preferable as it significantly decreases the cost of sampling full-torsion points, and only increases the cost of computing $\mathfrak{a} * E$ by a marginal amount.

The points T_+, T_- on the starting curve E_0 can be precomputed and considered public parameters, but for the public-key curves they must be computed in real time. We include the computation of these points in the key generation, and include them in the public key, which makes the shared-secret derivation completely constant-time and deterministic. The key generation is then the only part that does not run in strictly constant wall-clock time (yet is implemented following the constant-time paradigm), but is still made deterministic by sampling points in a pre-defined order. As we describe in Section 4, these points can be

represented in a very compact form, which increases public-key sizes by only a few bits. We further emphasize that in order to avoid active attacks, the shared-key computation must validate these transmitted points to be full-torsion points.

Following the SQALE implementation [24], we use the optimal strategy approach from [26] to efficiently evaluate the class group action.

3.3 Parameters for CTIDH

As mentioned above, the instantiations of dCSIDH that we use are designed as dummy-free and deterministic algorithms, in order to avoid potential issues with randomness and dummy operations. However, these choices induce significant computational overhead. Therefore, we additionally give performance results for CTIDH [5], the fastest available constant-time implementation of CSIDH (allowing randomness and dummy operations), at the same security levels so that we can compare performance. Note that [5] only reports performance results for 512-bit and 1024-bit primes.

For the parameter sizes considered in this work, we thus use the same primes as in the dCSIDH case (see Table 1). This allows for a simple comparison of the two approaches, since both implementations use the same finite field arithmetic (see Section 5). On the other hand, it is unclear which parameters are optimal for CTIDH with the given prime sizes. A larger number of small prime factors ℓ_i in the factorization of p+1 can be beneficial, since the combinatorial advantage of CTIDH batching increases with the number of available prime degrees. On the other hand, this would mean that we have to include larger ℓ_i , and therefore compute more expensive large degree isogenies. Furthermore, the choice of CTIDH parameters, i.e., batches and norm bounds, becomes more challenging at larger prime sizes. We thus leave the exploration of optimal CTIDH parameters for large primes as future work.

For the given primes, we use the greedy algorithm from [5] for determining these additional parameters, adapted to the case of the cofactor f > 4. On input of the primes ℓ_i and a fixed number of batches, the algorithm searches for a locally optimal way of batching the primes, and according norm bounds, such that the expected number of field multiplications per group action evaluation is minimized. However, for the parameter sizes in this work, the greedy search becomes increasingly inefficient. We could thus only run searches for a small set of potential batch numbers, especially for the larger parameters. We obtained these potential inputs by extrapolating from the data of smaller parameter sizes from [5] and slightly beyond. For concrete parameter choices, we refer to our software. Note that the choice of a different number of batches could improve the results, but an exhaustive search using the greedy algorithm seems out of reach.

Apart from the parameters and batching setup, our CTIDH implementation uses the algorithms and strategies from [5]. We remark that CTIDH could in theory also be implemented in a dummy-free or deterministic way. [5] presents an algorithm that avoids dummy isogenies, but points out that the Matryoshka isogenies require dummy operations by design. Thus, the current techniques do not allow for a dummy-free implementation of CTIDH. Further, the design of

a deterministic variant of CTIDH requires some adaptions, such as computing multiple isogenies per batch in a single round. We leave the design and analysis of such an implementation for future work.

4 Optimizing dCSIDH and CTIDH

Given the parameter choices from Section 3, we describe the high-level optimizations we apply for dCSIDH and CTIDH. Note that apart from the improved public key validation, we use the standard CTIDH implementation from [5] extended to the parameter sizes from Section 3.3. For dCSIDH, we present several improvements in Section 4.2.

4.1 Supersingularity verification

For the prime choices from Section 3.1, we need to adapt the supersingularity verification from [22]. In particular, given primes with cofactor $\log f > \frac{1}{2} \log p$, both algorithms discussed in [22, Alg. 1 and Alg. 3] to test supersingularity of a public key E_A do not work.

Note that these supersingular tests, verify whether $\#E_A(\mathbb{F}_p) = p+1$, by showing that there is a point P with large enough order $N \mid p+1$. Both algorithms start by sampling a random point P, followed by a multiplication by the cofactor $P \leftarrow [f]P$, and then by checking whether the resulting point has ℓ_i -torsion. This is done by calculating if $[\prod_{j\neq i}\ell_j]P \neq \mathcal{O}$ and $[\prod \ell_j]P = \mathcal{O}$. If the random point P has ℓ_i -torsion for enough ℓ_i such that their product $\prod \ell_i \geq 4\sqrt{p}$, then in the Hasse interval $p+1-2\sqrt{p} \leq \#E_A(\mathbb{F}_p) \leq p+1+2\sqrt{p}, \ p+1$ is the only possible multiple of its order ord(P). This implies that $\#E_A(\mathbb{F}_p) = p+1$. Unfortunately, this approach cannot be applied to our setting, because for primes where $\log f > \frac{1}{2} \log p$, even a point with ℓ_i -torsion for all i does not reach the threshold $4\sqrt{p}$, as $\log(\prod \ell_i) = \log p - \log f \leq \frac{1}{2} \log p$. We conclude that due to the large cofactors included in the primes targeted in this work, [22, Alg. 1 and Alg. 3] cannot perform a sound supersingularity test within our setting.

Luckily, in the primes as above, where $f=2^k$, we can improve this algorithm to verify supersingularity: Instead of verifying that the order of a random point P has enough ℓ_i -torsion, we verify P has 2^k -torsion. When $\log f = k > \frac{1}{2} \log p$, verifying that P has 2^k -torsion implies that E_A must be supersingular by the same logic as above. Furthermore, for Montgomery curves E_A , we can sample P directly from $E_A(\mathbb{F}_p) \setminus [2]E_A$ by picking a point with rational non-square x-coordinate [27]. This ensures we always sample P with maximum 2^k -torsion. Using x-only arithmetic, we only have to keep track of x_P . We name this approach to verify supersingularity VeriFast, as described in Algorithm 2.

VeriFast can be performed deterministically or probabilistically: Given a point with rational non-square x-coordinate, the algorithm always returns $v_2 = k$ in case of supersingularity. Otherwise, any random point is likely to have v_2 close to k, and hence still verifies supersingularity if the cofactor is a few bits larger than $4\sqrt{p}$. For the probabilistic approach, we pick $x_P = 2 \in \mathbb{F}_p$, hence P = (2, -),

Algorithm 2 VeriFast: Supersingularity verification for primes with cofactor $2^k > 4\sqrt{p}$.

```
Input: A \in \mathbb{F}_p defining a curve E_A

Output: true or false, verifying the supersingularity of E_A

1: x_P \leftarrow 2, v_2 \leftarrow 1

2: x_P \leftarrow [\frac{p+1}{f}]x_P

3: while x_P \neq \mathcal{O} and v_2 < k do

4: x_P \leftarrow \text{xDBL}(x_P), v_2 \leftarrow v_2 + 1

5: end while

6: if v_2 > 1 + \log p/2 and x_P = \mathcal{O} then

7: return true

8: end if

9: return false
```

for all supersingularity checks. This has the advantage that multiplication by 2 can be performed as a simple addition, and hence, $x_P=2$ optimizes the arithmetic in the computation of $x_P \leftarrow [\frac{p+1}{f}]x_P$. Furthermore, the bound $4\sqrt{p}$ can be improved to $2\sqrt{p}$ as this still implies p+1 is the only multiple in the Hasse interval. VeriFast is faster than any of the analyzed algorithms in [6], with a cost of $\mathcal{O}(\log p)$. More specifically, it requires a scalar multiplication by a scalar of $\log p - k$ bits and (at most) k point doublings, where $f = 2^k$ is the cofactor. In comparison to Doliskani's test [6, 29], also of complexity $\mathcal{O}(\log p)$, we have the advantage that we can stay over \mathbb{F}_p . The condition that $f > 1 + \log p/2$ holds for our primes p5120 and beyond. More importantly, even with the probabilistic approach, for these primes the probability to sample a point that does not have large enough 2^z -torsion is lower than 2^{-256} . For the primes where $f \leq 1 + \log p/2$, we can still use the 2^f -torsion, as in VeriFast, but we are required to also verify some ℓ_i -torsion to cross the bound $2\sqrt{p}$. A comparison of performance between VeriFast and previous methods is given in Table 2, showing VeriFast is 28 to 38 times as fast for large primes.

Table 2: Benchmarking results for supersingularity verification using VeriFast for primes with cofactor $\log f > \frac{1}{2} \log p$. Results of [24] added for comparison. Numbers are median clock cycles (in gigacycles) of 1024 runs on a Skylake CPU.

	p5120	p6144	p8192	p9216
VeriFast	0.53	0.81	1.88	2.54
SQALE [24]	14.90	27.65	67.79	96.99

4.2 Optimized dCSIDH public keys

As described in Section 3.2, dCSIDH is dummy-free and deterministic by using secret key exponents $e_i \in \{-1, 1\}$, and public keys of the form (A, T_+, T_-) . Recall, T_{+} and T_{-} are full-torsion points that can be used to perform positive steps l_{i}^{+1} and negative steps l_i^{-1} respectively. For sampling suitable points T_+ and T_- for public keys during key generation, we use the Elligator map $(A, u) \mapsto (T_+, T_-)$ from [23], with Montgomery parameter $A \in \mathbb{F}_p$ and an Elligator seed $u \in \mathbb{F}_p$. The output of Elligator is exactly such a pair of points T'_{+} and T'_{-} , although they might not be full-torsion, that is, their respective orders might not be divisible by all ℓ_i . Let P be either T_+ or T_- . To efficiently determine if P is a full-torsion point, we follow the usual product-tree approach that was also applied for public key validation in [22]. This requires us to compute $\left[\frac{p+1}{\ell_i}\right]P$ for each ℓ_i , and checking that these points are not equal to the point at infinity. In order to obtain a deterministic algorithm, we try Elligator seeds from a pre-defined sequence (u_1, u_2, \ldots) until we find full-torsion points T_+ and T_- . To determine which of the points T_{\pm} is T_{+} resp. T_{-} , Elligator requires a Legendre symbol computation. In the case of our proposed dCSIDH configuration with public inputs A and u, we can use a fast non-constant-time algorithm for the Legendre symbol computation as the one presented in Hamburg [33].

Thus, a dCSIDH public key consists of an affine Montgomery coefficient $A \in \mathbb{F}_p$, and an Elligator seed $u \in \mathbb{F}_p$ such that elligator(A, u) returns two full-torsion points T_+ and T_- on E_A . We choose the fixed potential values for u small to get a public key (A, u) of only $\log_2(p) + \varepsilon$ bits for small $\varepsilon > 0$.

Finally, a user has to verify such a public key (A, u). For A, we verify E_A is supersingular as described in Section 4.1. For u, we verify that it generates two full-torsion points T_+ and T_- , by ensuring at the computation of each step $\mathfrak{l}_i^{\pm 1} * E$ that the correct multiple of both T_+ and T_- are not the point at infinity (i.e., both have order ℓ_i) regardless of which point we use to compute the step.

Remark 1. An alternative to finding and including an Elligator seed $u \in \mathbb{F}_p$ in the public key is to find and include small x-coordinates x_+ and x_- that define full-torsion points $T_+ = (x_+, -)$ and $T_- = (x_-, -)$. Information-theoretically, u and the pair (x_+, x_-) share similar probabilities (to generate full-torsion points) and hence their bitlengths should be comparatively small. One advantage of x_+ and x_- is that they can be found individually, which should speed up their search. We choose, however, the more succinct approach using u and Elligator.

5 Implementation

In this section, we describe the optimization steps at the level of field arithmetic to speed up both variants of CSIDH we consider. First and foremost, to enable a fair comparison, we implement a common code base for dCSIDH and CTIDH. Besides sharing the same field arithmetic, both instantiations of CSIDH share all the underlying functions required for computing the group action. However, some

required parameters and the strategy within the group action strongly differ between dCSIDH and CTIDH. In the case of dCSIDH, the group action strategy and all the required parameters are based on the implementation provided by [24]. In the case of CTIDH, we generate the batching and other parameters using the methods provided by [5].

5.1 Low-level approaches for the field arithmetic layer

For the underlying field arithmetic, we implement three different approaches. They all share the representation of integers in radix 2^{64} and use Montgomery arithmetic for efficient reductions modulo p.

- 1. To establish a performance baseline, our first method uses the low-level functions for cryptography (mpn_sec_) of the GNU Multiple Precision Arithmetic Library (GMP). Modular multiplication uses a combination of mpn_sec_mul and mpn_add_n to implement Montgomery multiplication, i.e., interleaving multiplication with reduction. We refer to this first approach as GMP.
- 2. The second approach extends the optimized arithmetic from [22], using the MULX instruction, going from 512-bit and 1024-bit integers to the larger sizes we consider in this paper. Here, we also interleave multiplication with reduction; we generate code for all field sizes from a Python script. We refer to this second approach as **OpScan**.
- 3. Our third strategy uses Karatsuba multiplication [36] together with the MULX optimizations used in our second approach. We describe this strategy, and in particular an optimized reduction for primes of 5120 bits and above, in more detail in Section 5.2. We refer to this third approach as Karatsuba.

We follow the earlier optimization efforts for CSIDH from [5, 22, 24] and focus on optimizing our code primarily on Intel's Skylake microarchitecture. More specifically, we perform all benchmarks on one core of an Intel Core E3-1260L (Skylake) CPU with hyperthreading and TurboBoost disabled. An overview of (modular) multiplication performance of the three approaches for the different field sizes is given in Table 3. In the following, we will focus on describing the fastest of the three strategies mentioned above, i.e., Karatsuba, in more detail.

5.2 Optimized field arithmetic using MULX and Karatsuba

We present scripts to generate optimized code using the **Karatsuba** approach, based on the **OpScan** approach. More precisely, compared to the **OpScan** approach, we achieve speedups for multiplication, squaring, and reduction.

Multiplication. The implementation of Karatsuba follows careful considerations to optimize performance. To improve efficiency, we select a breakout level into a MULX-based schoolbook multiplication with a maximum of 9×9 limbs. By choosing this threshold, the implementation aims to strike a balance between utilizing the benefits of Karatsuba's divide-and-conquer strategy and minimizing

Table 3: Benchmarking results for multiplication and reduction. Numbers are median clock cycles of 100000 runs on a Skylake CPU. Note that for the **OpScan** and the **GMP** approach, we can only provide clock cycles for multiplication including reduction, due to the interleaved Montgomery reduction.

Prime	GMP	OpScan	Karatsuba			
	mult + redc	mult + redc	mult	redc	mult + redc	
p2048	8662	4538	1442	2648	4090	
p4096	34030	20318	4981	9777	14758	
p5120	51671	33676	8601	6528	15129	
p6144	74338	53746	10210	9517	19727	
p8192	131858	92793	17073	17295	34268	
p9216	168375	118302	20248	19709	39957	

the overhead of stack operations. This leads to the following number of layers of Karatsuba: 2, 3, 4, 4, 4, and 4 for the cases p2048, p4096, p5120, p6144, p8192, and p9216, respectively. To further enhance the speed of the implementation, the assembly code avoids function calls. By generating the assembly code dynamically, the implementation can adapt to different prime sizes and adjust the multiplication algorithm accordingly.

Squaring. For squaring, we take advantage of the fact that some partial products $(a_i a_j)$ such that $i \neq j$ only need to be calculated once, and then accumulated/used twice. On the lowest level of Karatsuba, where the schoolbook multiplication takes place, we implement a squaring function with the corresponding savings based on lazy doubling method [43] by adapting the assembly code of the squaring function of the GMP library. For a given n, the implemented method achieves the lower bound of $\frac{n^2-n}{2}+n$ required multiplications. Furthermore, we save additions on the higher levels of Karatsuba by reusing calculated values. However, as shown in Table 4, due to the chosen breakout into schoolbook multiplication and the number of available registers, the effort for dealing with the carry chains only leads to a maximum speedup of 17%. Adding a layer of Karatsuba to reduce the number of limbs for the schoolbook multiplication leads to a speedup at this level. Overall, however, extra layers negate speed-ups gained from reducing limbs.

Montgomery reduction. For the cases $p \in \{p5120, p6144, p8192, p9216\}$, the reduction is calculated according to the *intermediate Montgomery reduction* [4]. For this, we use Montgomery-friendly primes of the form $p = f \cdot \prod_{i=1}^{n} \ell_i - 1$ with the cofactor $f = 2^{e_2}$ where $e_2 \ge \log_2(p)/2$. Table 1 shows the respective values for f and accordingly e_2 for all chosen prime numbers.

As shown in Algorithm 3, the basic idea of this reduction is to perform two Montgomery-reduction steps modulo 2^{e_2} instead of n steps modulo 2^w as in the standard Montgomery reduction. Based on this reduction approach, we can

Table 4: Benchmarking results for multiplication and squaring for the **Karatsuba** approach. Numbers are median clock cycles of 100000 runs on a Skylake CPU.

Prime	multiplication	squaring
p2048	1442	1230
p4096	4981	4431
p5120	8601	7990
p6144	10210	9120
p8192	17073	15050
p9216	20248	19197

further apply the available Karatsuba-based multiplication when calculating $q_0 \times \alpha$ and $q_1 \times \alpha$ (see Line 2 and 4 in Algorithm 3), leading to further speedups.

Algorithm 3 Intermediate Montgomery reduction for $p = 2^{e_2}\alpha - 1$ with $e_2 \ge \log_2(p)/2$

```
Input: 0 \le a < 2^{e}p

Output: r = a2^{-2e_2} \mod p and 0 \le r < p

1: q_0 \leftarrow a \mod 2^{e_2}

2: r_0 \leftarrow (a - q_0)/2^{e_2} + q_0 \times \alpha

3: q_1 \leftarrow r_0 \mod 2^{e_2}

4: r \leftarrow (r_0 - q_1)/2^{e_2} + q_1 \times \alpha

5: r' \leftarrow r - p + 2^e

6: if r' \ge 2^e then

7: r \leftarrow r' \mod 2^e

8: end if

9: return r
```

For the cases $p \in \{p2048, p4096\}$, the respective primes cannot fulfill the described requirements. Hence, we implement the *word version of the Montgomery reduction* from [4] for these cases. The complexity of Algorithm 4 is dominated by multiplications by α in Line 4. Compared to the standard Montgomery reduction, this approach reduces the number of limbs to be multiplied depending on the value of e_2 . We show the results for the corresponding reduction in Table 3.

5.3 Performance results

We demonstrate the performance increase due to the high-level improvements from Section 4 and the low-level improvements from Section 5.2 for dCSIDH and CTIDH in Table 5. We compare our results to [24], the only other available implementation of CSIDH for similar parameters listing performance numbers.

Algorithm 4 Word version of the Montgomery reduction if $p = 2^{e_2}\alpha - 1$

```
Input: 0 \le a < p\beta^n

Output: r = a\beta^{-n} \mod p and 0 \le r < p

1: r \leftarrow a

2: for i = 0 to n - 1 do

3: r_0 \leftarrow r \mod \beta

4: r \leftarrow (r - r_0)/\beta + r_0 \times \alpha 2^{e_2 - w}

5: end for

6: r' \leftarrow r + (\beta^n - p)

7: if r' \ge \beta^n then

8: r \leftarrow r' - \beta

9: end if

10: return r
```

For parameter sizes above p5120, our implementation of dCSIDH is between 55% and 60% faster than SQALE (dummy-free), and CTIDH consistently achieves a speed-up of almost 75% compared to SQALE (OAYT).

Table 5: Benchmarking results for performing a group action for dCSIDH and CTIDH, excluding key validation. Results for the dummy-free and OAYT version of [24] added for comparison. Numbers are median clock cycles (in gigacycles) of 1024 executions on a Skylake CPU.

_	p2048	p4096	p5120	p6144	p8192	p9216
dCSIDH	7.48	34.64	31.80	47.47	127.57	219.09
SQALE (dummy-free)		39.35	73.57	117.57	322.57	475.64
CTIDH	2.21	11.11	11.26	17.13	43.65	68.78
SQALE (OAYT)	_	23.21	44.56	74.88	199.15	292.41

6 Non-Interactive Key Exchange in Protocols

Diffie-Hellman (DH) key exchange is probably the most well-known example of a NIKE protocol, even if it is often used as a "simple" interactive key exchange. One such example is TLS, where ephemeral DH key exchange is authenticated via a signature. This key exchange can be replaced with a KEM, as shown in [13]. Experiments by Google and Cloudflare [14, 41, 42] used the same approach.

However, in two scenarios the inherently interactive character of a KEM creates issues for protocol designers. When used with long-term keys (and a suitable PKI), a NIKE allows a user Alice to send an authenticated ciphertext to an *offline* user Bob. Signal's X3DH handshake [47] is a notable example using

this feature of NIKEs. Indeed, [15] shows that a naive replacement of the DH operations by KEMs does not work.

In the early stages of the development of TLS 1.3, Krawczyk and Wee proposed OPTLS [38], a variant that uses DH key exchange not only for ephemeral key exchange, but also for authentication. Many elements of this proposal, made it into the eventual RFC8446 [54]. Though the standard reverted to handshake signatures, the idea lives on in an Internet Draft [55].

As Kuhnen pointed out, OPTLS does use the non-interactive property of DH [39]. As part of the ephemeral key exchange, the client sends their ephemeral DH public key. For authentication, the server takes this ephemeral key share and combines it with their long-term DH key. The obtained shared secret is used to compute a MAC which is used in place of the signature in the CertificateVerify message. This computation proves the server's possession of the long-term secret key corresponding to the public key in the certificate. The client can compute the same shared secret by combining its ephemeral secret DH key with the certified public key, and thus verify the MAC.

6.1 Post-Quantum TLS without signatures

In a naive instantiation of an OPTLS-like protocol with KEMs, we require an additional round-trip. To compute the authentication message, the server needs to first receive the ciphertext that was encapsulated against the long-term public key held in its certificate—which the client can not send before having received it from the server. The KEMTLS proposal by Schwabe, Stebila, and Wiggers avoids this issue partially by letting the client already transmit data immediately after computing and sending the ciphertext to the server [58]. This relies on the fact that any keys derived from the shared secret encapsulated to the server's long term key are *implicitly authenticated*. KEMTLS has the advantage of not having to compute any typically expensive and/or large post-quantum signatures during the handshake protocol. Only the variant that assumes the client already has the server's public key, for example through caching, can achieve a protocol flow that is similar to OPTLS and TLS 1.3 [57]. In that flow, the server can send authenticated data immediately on their first response to the client.

However, as CSIDH does provide post-quantum NIKE we can use it to instantiate post-quantum OPTLS and avoid any online post-quantum signatures. Because OPTLS immediately confirms the server's authenticity, its handshake has the same number of transmissions of messages as in TLS 1.3 and there is no need to rely on implicit authentication.

6.2 Benchmarking set-up

Integration into Rustls. To investigate the performance of OPTLS with CSIDH, we integrate our optimized implementations into the implementation and the measurement framework of the authors of KEMTLS. As a side effect of this work, we also provide a Rust wrapper around our C implementations.

We add OPTLS to the same modified version of Rustls [11] used to implement KEMTLS. This allows us to straightforwardly compare to KEMTLS and TLS 1.3 handshakes instantiated with post-quantum primitives.

Ephemeral key generation. Because CSIDH key generation takes a lot of time, we implement caching of ephemeral keys. This reduces the forward secrecy; but it emulates a best-case scenario for CSIDH-based OPTLS in which the keys are generated "offline", outside the handshake context. We exclude all first TLS handshakes from clients and servers from our measurements, to exclude this key generation time: in the *pregen* OPTLS instances, all subsequent handshakes use the same public key material. In the *ephemeral* OPTLS instances, we generate ephemeral keys in each handshake.

Measurement setup. We run all TLS handshake experiments on a server with two Intel Xeon Gold 6230 CPUs, each featuring 20 physical cores. This gives us 80 hyperthreaded cores in total. For these experiments, we do not disable hyperthreading or frequency scaling, as these features would also be enabled in production scenarios. We run 80 servers and clients in parallel, as each pair of client and server roughly interleave their execution. We collect 8000 measurements per experiment. Every 11 handshakes, we restart the client and server, so that we measure many ephemeral keys even in the scenarios that use ephemeral-key caching. We exclude the first handshake from the measurements to allow for cache warm-up and ephemeral-key generation in the caching scenario.

As in the KEMTLS papers [57, 58], we measure the performance of the different TLS handshakes when run over two network environments: a low-latency 30.9 ms RTT, 1000 Mbps and a high-latency 195.5 ms, 10 Mbps network connection. The latency of the former represents a continental, high-bandwidth connection, while the latter represents a transatlantic connection.

6.3 Benchmarking results

In Table 6, we compare OPTLS with dCSIDH and CTIDH with the performance of instantiations of KEMTLS and TLS 1.3.

Comparing the sizes of the handshakes, OPTLS requires fewer bytes on the wire, as it only needs to transmit two ephemeral public keys and one static public key (and the CA signature). KEMTLS requires an additional ciphertext, and TLS an additional signature.

In OPTLS, like in TLS 1.3, the client receives the server's handshake completion message SFIN first and then sends the CFIN message (and its request) immediately after. In KEMTLS, SFIN and full server authentication is received a full round-trip after CFIN is received. However, it is clear that the runtime requirements of dCSIDH are almost insurmountable, even for the smallest parameters (p2048). Even CTIDH, which is much more efficient, is orders of magnitude slower than the KEMTLS and OPTLS instances. If the more conservative p4096 prime is required for NIST level 1 security, even CTIDH handshakes do not complete in under 30 seconds. Due to better reduction, the p5120 prime performs roughly on par with p4096, while providing NIST level 2 security in the aggressive analysis.

Table 6: Public key cryptography transmission sizes in bytes and time in seconds until client receives and sends Finished messages for OPTLS, TLS 1.3 and KEMTLS.

				Handshake latencies (RTT, link speed)				
		Transmission		$30.9\mathrm{ms},1000\mathrm{Mbps}$		$195.5\mathrm{ms},10\mathrm{Mbps}$		
		KEX	Auth	SFIN recv	CFIN sent	SFIN recv	CFIN sent	
	dCSIDH p2048	1024	1178	24.476	24.476	24.185	24.185	
OPTLS	CTIDH p2048	1024	1178	7.350	7.350	7.189	7.189	
(pregen)	CTIDH p4096	2048	1690	36.189	36.189	36.032	36.032	
	CTIDH p5120	2560	1946	35.624	35.624	35.417	35.417	
OPTLS (ephemeral)	dCSIDH p2048	1024	1178	43.705	43.705	43.295	43.295	
	CTIDH p2048	1024	1178	10.059	10.059	9.816	9.816	
	CTIDH p4096	2048	1690	49.868	49.868	49.660	49.660	
	CTIDH p5120	2560	1946	49.487	49.487	49.023	49.023	
TLS	Kyber512–Falcon512	1568	2229	0.064	0.064	0.428	0.428	
	Kyber512–Dilithium2	1568	4398	0.063	0.063	0.520	0.520	
	Kyber 768-Falcon 1024	2272	3739	0.065	0.065	0.498	0.498	
KEMTLS	Kyber512	1568	2234	0.094	0.063	0.593	0.396	
	Kyber768	2272	2938	0.094	0.063	0.598	0.400	

All instantiations use Falcon-512 for the certificate authority; the CA public key is not transmitted. Bytes necessary for authentication includes 666 bytes for the Falcon-512 CA signature on the server's certificate.

As discussed for (KEM)TLS in [32], for constrained environments, such as 46 kbps IoT networks, in certain scenarios the transmission size can become the dominant factor instead of computation time. However, with the results shown here, we expect the environments in which CSIDH-based OPTLS instances are competitive to be very niche.

Interestingly, the CSIDH experiments run on the high-latency, low-bandwidth networks show slightly lower latencies than those on the high-bandwidth, low-latency network. We suspect that this is due to an interaction with the TCP congestion control algorithm's transmission windows.

7 Conclusion and future work

In this paper, we presented low-level and high-level optimizations for CSIDH at larger parameter sets, focusing on deterministic and dummy-free behavior in dCSIDH, and on speed in CTIDH. These optimizations achieve impressive results on their own; dCSIDH is almost twice as fast as the state-of-the-art, and CTIDH, dropping determinism, is again three times as fast as dCSIDH. Further optimizations of the field arithmetic, i.e. by utilizing the vector processing capabilities of modern processors, might lead to additional speed-ups.

Nevertheless, when integrated into OPTLS, both implementations still have too-large handshake latency in comparison to TLS or KEMTLS using lattice-based KEMs. We conclude that the reduced number of roundtrips, through the non-interactive nature of CSIDH, does not make up for the performance hit.

However, for truly non-interactive settings that cannot replace NIKEs by KEMs, the performance of CSIDH is sufficient even at high-security levels. This includes, for example, using CSIDH in X3DH [47] for post-quantum Signal, as it would incur a delay of seconds only when sending the first message to another user who is assumed to be offline.

Unless significant performance improvements occur for CSIDH in large parameter sets, or the quantum-security debate shifts in favor of 512- to 1024-bits parameter sets, we conclude that CSIDH is unlikely to be practical in real-world applications, outside of those that specifically require NIKEs.

It will be interesting to investigate how CSIDH and SWOOSH—the only two current proposals for a post-quantum NIKE—compare in a protocol context. There is no full implementation of SWOOSH, yet; the cycle counts reported in [30] are for the passively-secure core component only. Based on the available figures it seems likely that SWOOSH outperforms CSIDH with the large parameters we consider in this paper *computationally*, but that key sizes are much smaller for CSIDH.

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